

| | | | | | | | | |
|--------|---|---|---|---|------------|-----|-----|-----|
| Bit | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| | | | | | SMODE[1:0] | | SEN | |
| Access | R | R | R | R | R | R/W | R/W | R/W |
| Reset | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |

Bits 2:1 – SMODE[1:0] Sleep Mode

Writing these bits selects the sleep mode entered when the Sleep Enable (SEN) bit is written to '1', and the `SLEEP` instruction is executed.

| Value | Name | Description |
|-------|---------|-------------------------------|
| 0x0 | IDLE | Idle sleep mode enabled |
| 0x1 | STANDBY | Standby sleep mode enabled |
| 0x2 | PDOWN | Power-Down sleep mode enabled |
| other | - | Reserved |

Bit 0 – SEN Sleep Enable

This bit must be written to '1' before the `SLEEP` instruction is executed to make the MCU enter the selected sleep mode.