

Bit	7	6	5	4	3	2	1	0
	RUNSTDBY	OUTEN	INTMODE[1:0]		LPMODE	HYSMODE[1:0]		ENABLE
Access	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W
Reset	0	0	0	0	0	0	0	0

bit 6 OUTEN: Analog Comparator Output Pad Enable bit
Writing this bit to '1' makes the OUT signal available on the pin.

bits 5:4 INTMODE[1:0]: Interrupt Modes bits
Writing to these bits selects what edges of the AC output triggers an interrupt request.

Value	Name	Description
0x0	BOTHEDGE	Both negative and positive edge
0x1	-	Reserved
0x2	NEGEDGE	Negative edge
0x3	POSEDGE	Positive edge

bit 0 – ENABLE: Enable AC bit
Writing this bit to '1' enables the AC.