Bit	7	6	5	4	3	2	1	0
		CMP2EN	CMP1EN	CMP0EN	ALUPD	WGMODE[2:0]		
Access		R/W	R/W	R/W	R/W	R/W	R/W	R/W
Reset		0	0	0	0	0	0	0

bits 2:0 WGMODE[2:0]: Waveform Generation Mode bits

These bits select the Waveform Generation mode.

Value	Name	Description
0x0	NORMAL	Normal Operation mode
0x1	FRQ	Frequency mode
0x3	SINGLESLOPE	Single-Slope PWM mode
0x5	DSTOP	Dual-Slope PWM mode
0x6	DSBOTH	Dual-Slope PWM mode
0x7	DSBOTTOM	Dual-Slope PWM mode
Other	-	Reserved