

Bit	7	6	5	4	3	2	1	0
	GENERATOR[7:0]							
Access	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W
Reset	0	0	0	0	0	0	0	0

bits 7:0 GENERATOR[7:0]: Channel Generator Selection bits

Value		Description
...		
0x10-0x13	CCL_LUTn	Event channel connected to LUTn
0x20	AC0	Event channel connected to AC0
0x24	ADC0	Event channel connected to ADC0
...		