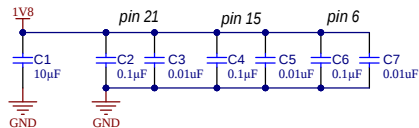
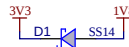
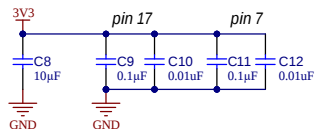


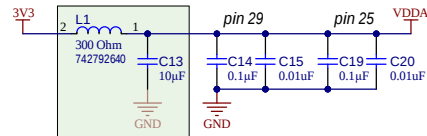
(i.e. 12V)



LAYOUT NOTE: Place one 0.01uF and one 0.1uF at each power pin. The 0.01uF must be closest to the pin.

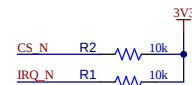
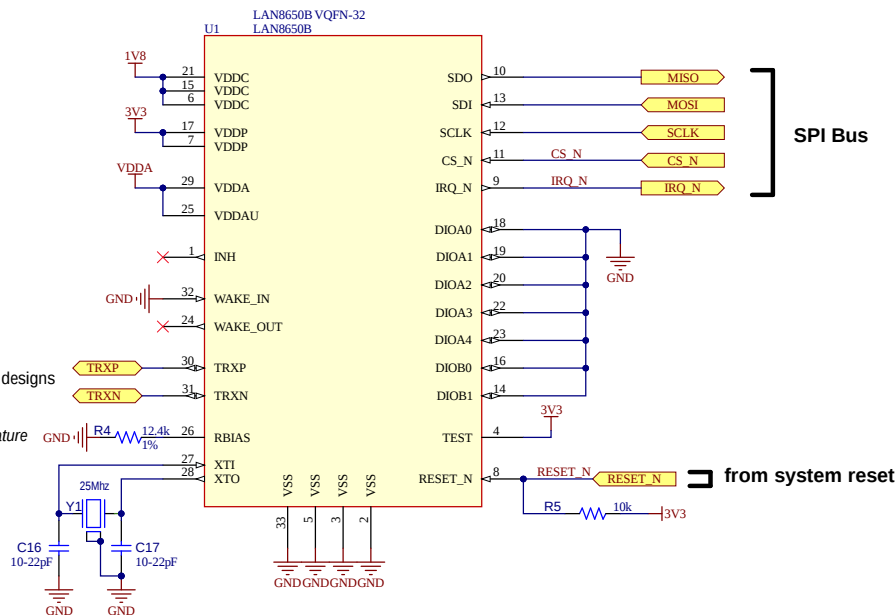


Ferrite beads are optional. If used, a bulk capacitor is recommended on the device side of the ferrite bead to dampen potential oscillation.



See AN1718 for latest BIN designs

RBIAS: +/- 1% over temperature



NOTE: Pullup on IRQ_N is optional depending on application

NOTE: If the system reset is unused, RESET_N pin can be tied directly to VDDP