AT28C256: Package Marking Information

28-lead PDIP	32-lead PLCC

28-lead PDIP

Topside
```
ATMEL
AT28C256@
%%U-19802C
YYWWNNN
```

Backside

32-lead PLCC

Topside
```
ATMEL
AT28C256@
%%U-19802C
YYWWNNN
```

Backside

28-lead SOIC

Topside
```
ATMEL 19802C
AT28C256@-%%U
YYWWNNN
```

Backside

Note: No backside marking on available packages

	%% = Access Time	@ = Write Endurance Rating	
	15: 150 ns	Blank: Standard (10K at 10 ms)	
		E: Extended (100K at 10 ms)	
		F: Fast Write (10k at 3 ms)	
		Lot Trace Code	
		YWWNNN: Lot Trace Code	
		Y: Year, WW: Work Week,	
		NNN: Assembly Trace Code	