

$\overline{\text{CS}}$

$t_{\text{WC}}^{(1)}$

SCK

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

WRITE Opcode (02h)

Address Bits A_7-A_0

Data In

SI

MSb

MSb

MSb

High-Impedance

SO

