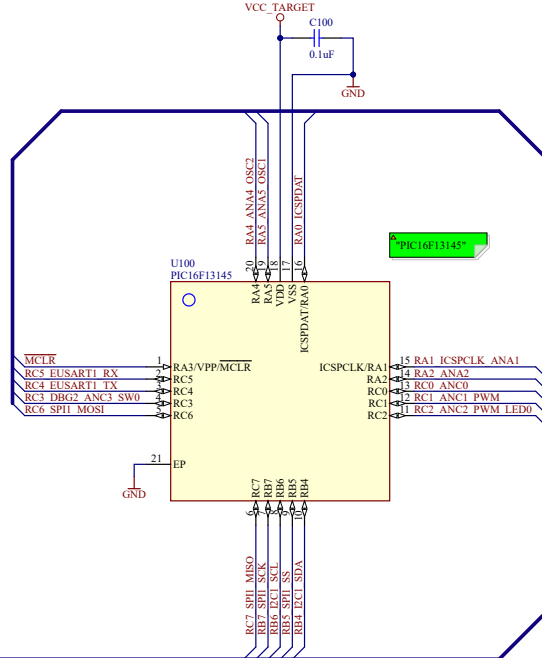
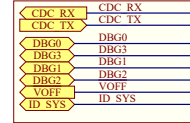


PIC16F13145

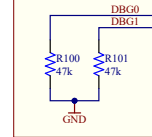


PIC16F13145		
Debugger	Name	Pin
CDC TX	EUSART1 RX	RC5
CDC RX	EUSART1 TX	RC4
DBG0	ICSPDAT	RA0
DBG1	ICSPCLK	RA1
DBG2	GPI00	RC3
DBG3	MCLR	RA3
VTG	1.8V - 5.5V	

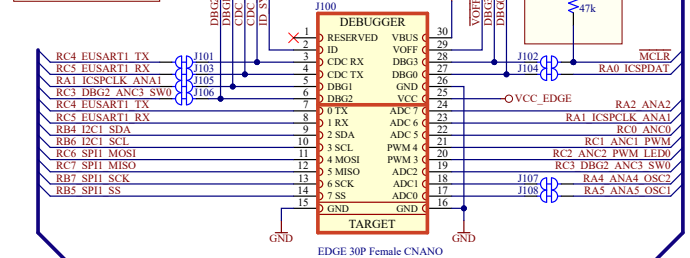
DEBUGGER CONNECTIONS



PROG/DEBUG Pull



MCLR Pull



NOTE on UART/CDC:

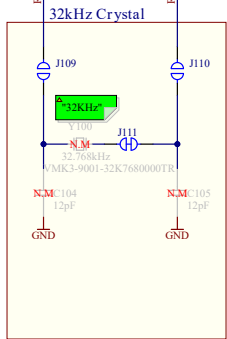
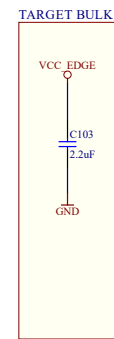
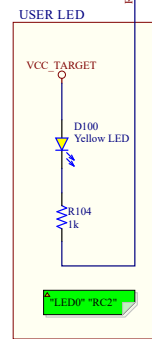
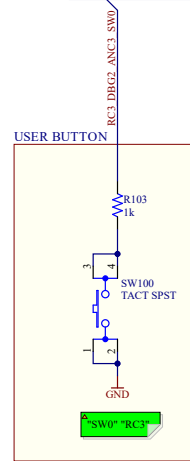
RX/TX on the header denotes the input/output direction of the signal respective to it's source.
 CDC TX is output from the DEBUGGER.
 CDC RX is input to the DEBUGGER.
 TX is output from the TARGET device.
 RX is input to the TARGET device.

NOTE on I2C:

No pull-ups on board. Pull-ups must be mounted close to client device(s).

NOTE on Crystal:

The crystal is optional and not populated by default.
 Crystal datasheet:
 Crystal = 9pF
 max ESR = 70kOhm
 Accuracy ±20ppm
 Selected in design after verification
 C = 12/12pF
 INFO: For best performance a 220k series resistor must be added between RA4 and Y100-2.



Project Owner:
TF

PCB Layout Contact:
VL

PartNumber:
EV06M52A

Project Title:
PIC16F13145 Curiosity Nano

Variant:
Default Assembly

Sheet Title:
Target MCU

Size:
A3

SCH #:
02-00913

Rev:
2

Date:
2023-11-30

PCB #:
04-12009

Rev:
1

Sheet
2 of 4

Designed with
Altium

File:
PIC16F13145 Curiosity Nano Target MCU.SchDoc