```
                    START

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    Write the instruction code        QSPI Configuration
    and/or the option code
      in QSPI_RICR

      Configure QSPI_IFR
   according to the targeted memory

                              No
        Scrambling
        required?

              Yes

     Configure QSPI_SKR and
          QSPI_SMR

   Wait until QSPI_SR.SYNCBSY=0,
     write QSPI_CR.UPDCFG,
   wait until QSPI_SR.SYNCBSY=0

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    Read Data Buffer in QSPI          Data Read
    system bus memory space

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  Wait until flag QSPI_SR.RBUSY=0     End of Frame

   Wait until QSPI_SR.SYNCBSY=0
              then
    wait until QSPI_SR.HIDLE=1
              then
      write QSPI_CR.LASTXFER

   Wait for flag QSPI_ISR.CSRA
   to rise by polling or interrupt

                    END
```