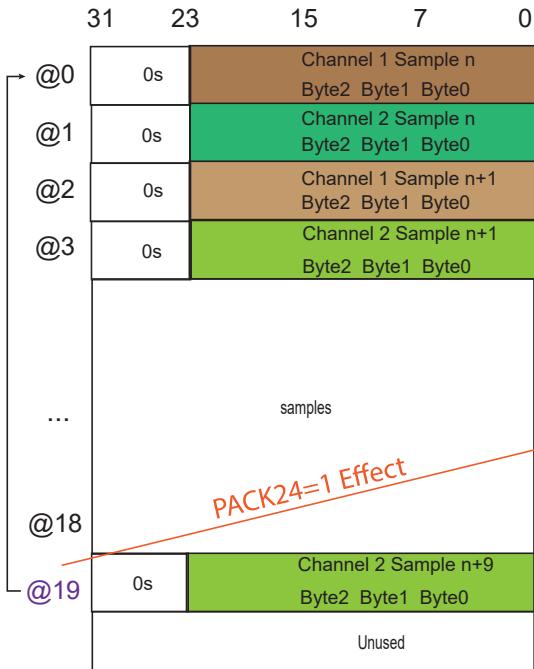
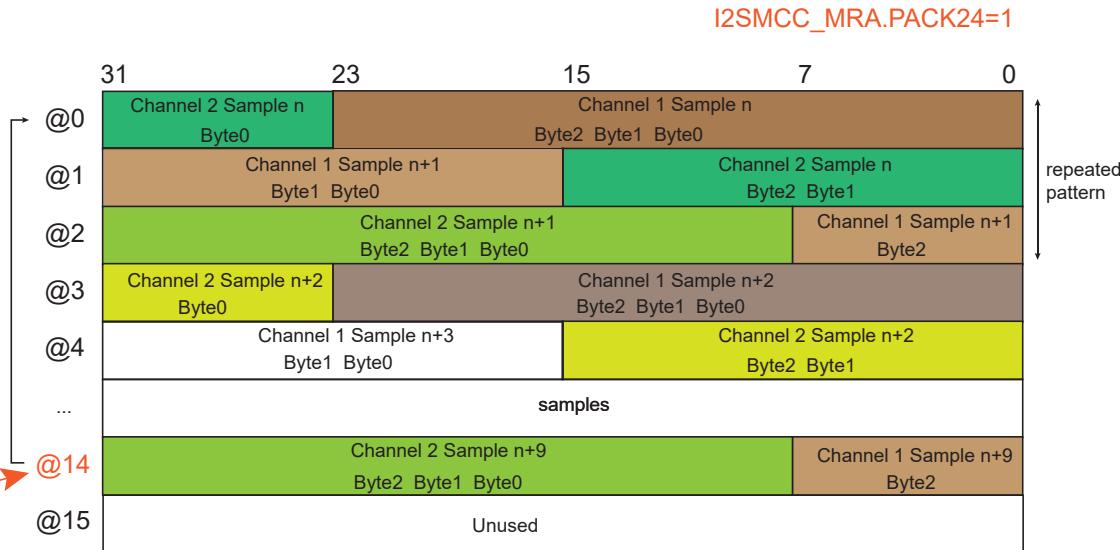


I2SMCC_MRA.DATALENGTH=1 (24 bits)

I2SMCC_MRA.PACK24=0



PACK24=1 Effect



I2SMCC_MRA.PACK24=1

Transmit: The system memory buffer must be transferred with word (32-bit) accesses to I2SMCC_THR.

Receive: The I2SMCC_RHR data must be transferred to system memory with word (32-bit) accesses.

Note: Buffers must be integer multiples of 3 words.

I2SMCC_MRA.DATALENGTH=2 (20 bits)

I2SMCC_MRA.PACK24=1

