START (S/Sr)	Address Header (0x7E,w)	Directed CCC Code (CCC,T)	Defining Byte (DF_Byte,T)	RESTART (Sr)	...

...	Target Address (TA,w/r)	Data Payload (DB,T)	STOP (P/Sr)

Legend
S = Start
Sr = Repeated Start
T = Transition Bit
TA = Target Address
DB = Data Byte
P = Stop