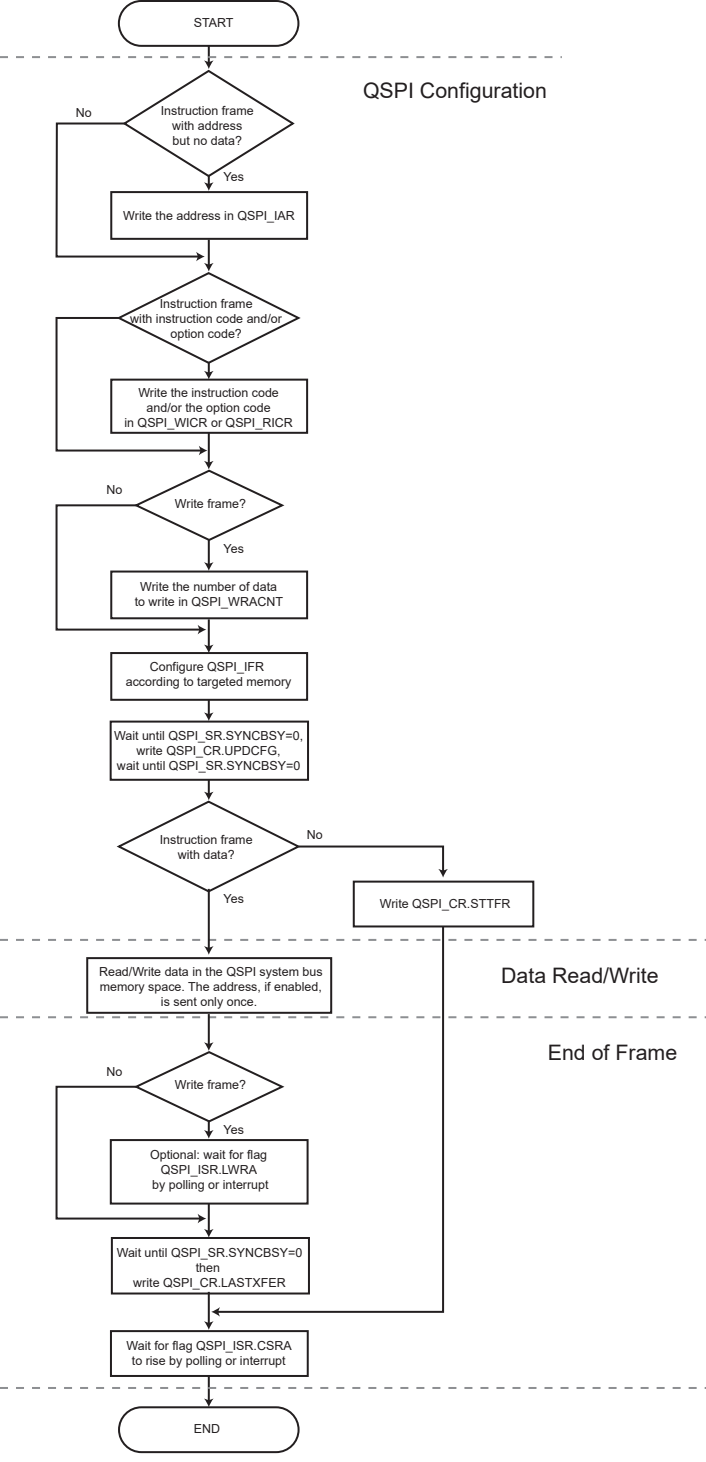
START
QSPI Configuration
Instruction frame with address but no data?
No
Yes
Write the address in QSPI_IAR
Instruction frame with instruction code and/or option code?
Write the instruction code and/or the option code in QSPI_WICR or QSPI_RICR
Write frame?
No
Yes
Write the number of data to write in QSPI_WRACNT
Configure QSPI_IFR according to targeted memory
Wait until QSPI_SR.SYNCBSY=0, write QSPI_CR.UPDCFG, wait until QSPI_SR.SYNCBSY=0
Instruction frame with data?
No
Yes
Write QSPI_CR.STTFR
Data Read/Write
Read/Write data in the QSPI system bus memory space. The address, if enabled, is sent only once.
End of Frame
Write frame?
No
Yes
Optional: wait for flag QSPI_ISR.LWRA by polling or interrupt
Wait until QSPI_SR.SYNCBSY=0 then write QSPI_CR.LASTXFER
Wait for flag QSPI_ISR.CSRA to rise by polling or interrupt
END