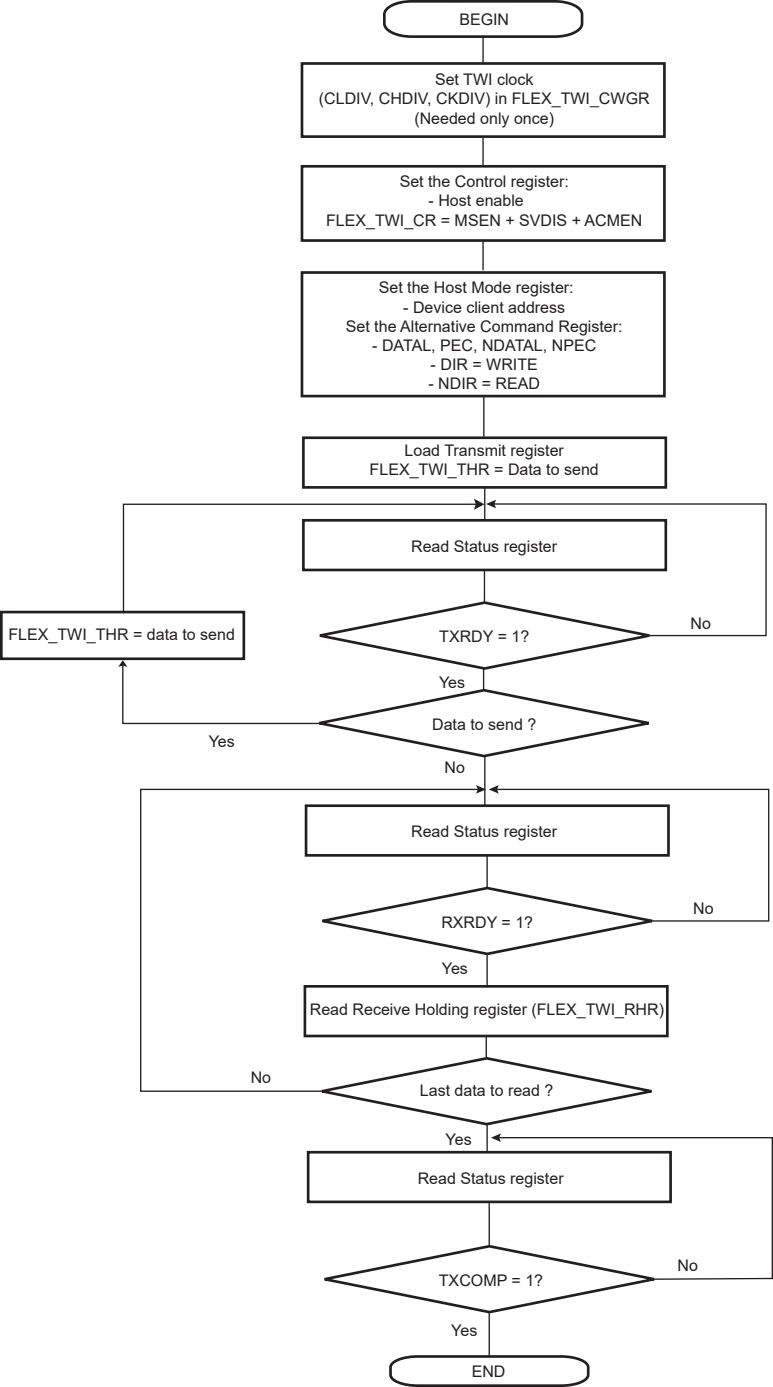
BEGIN
Set TWI clock
(CLDIV, CHDIV, CKDIV) in FLEX_TWI_CWGR
(Needed only once)
Set the Control register:
- Host enable
FLEX_TWI_CR = MSEN + SVDIS + ACMEN
Set the Host Mode register:
- Device client address
Set the Alternative Command Register:
- DATAL, PEC, NDATAL, NPEC
- DIR = WRITE
- NDIR = READ
Load Transmit register
FLEX_TWI_THR = Data to send
Read Status register
TXRDY = 1?
No
Yes
Data to send ?
Yes
FLEX_TWI_THR = data to send
No
Read Status register
RXRDY = 1?
No
Yes
Read Receive Holding register (FLEX_TWI_RHR)
Last data to read ?
No
Yes
Read Status register
TXCOMP = 1?
No
Yes
END