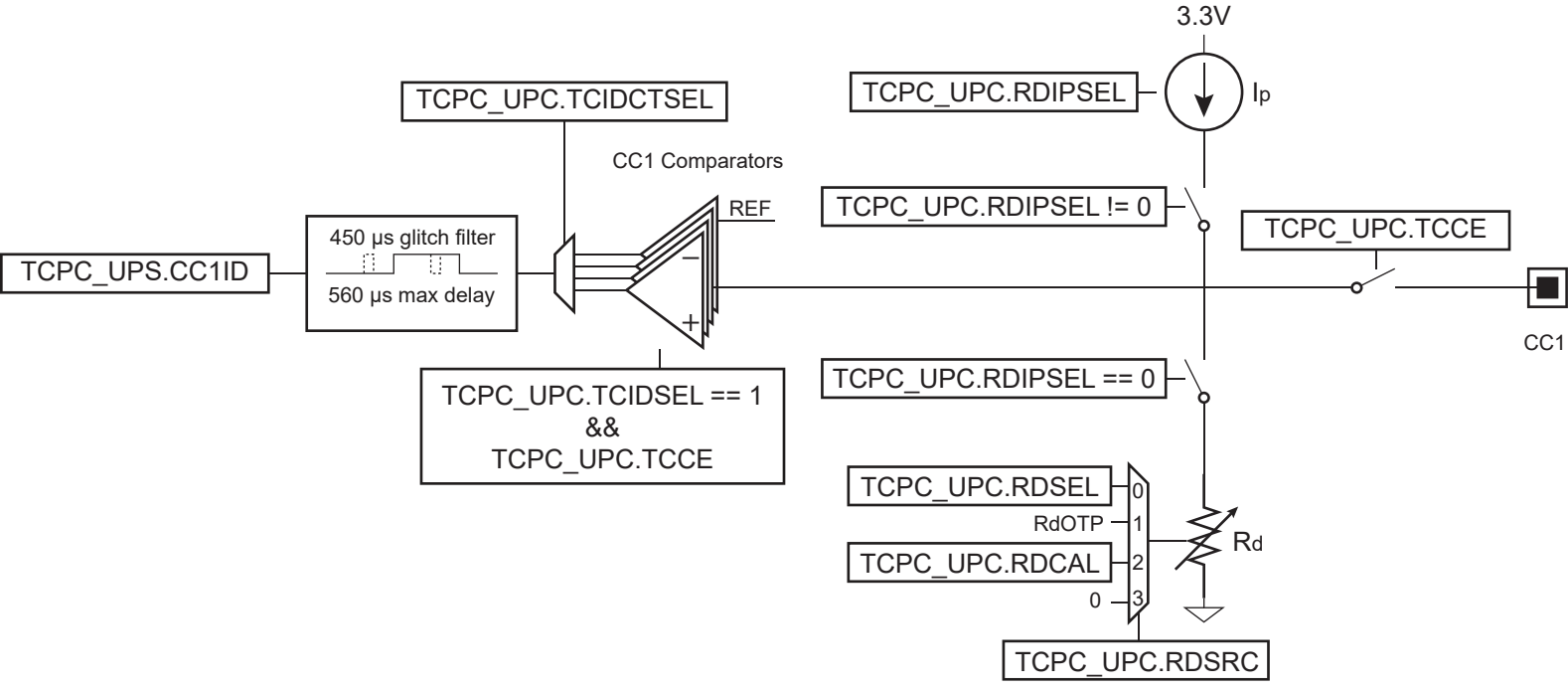
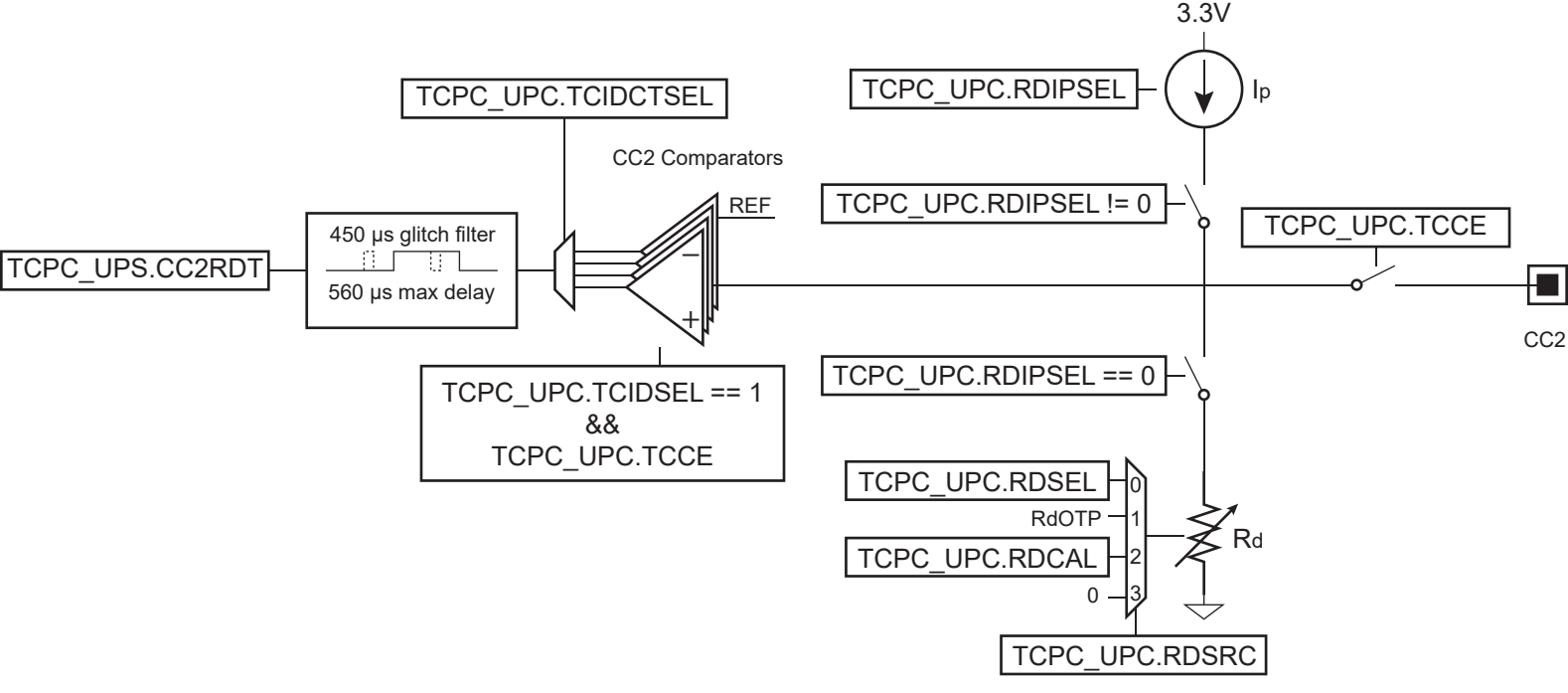

3.3V
TCPC_UPC.RDIPSEL
Ip
TCPC_UPC.TCIDCTSEL
CC1 Comparators
REF
TCPC_UPC.RDIPSEL != 0
TCPC_UPC.TCCE
TCPC_UPS.CC1ID
450 µs glitch filter
560 µs max delay
CC1
TCPC_UPC.RDIPSEL == 0
TCPC_UPC.TCIDSEL == 1
&&
TCPC_UPC.TCCE
TCPC_UPC.RDSEL
RdOTP
TCPC_UPC.RDCAL
0
0
1
2
3
Rd
TCPC_UPC.RDSRC


3.3V
TCPC_UPC.RDIPSEL
Ip
TCPC_UPC.TCIDCTSEL
CC2 Comparators
REF
TCPC_UPC.RDIPSEL != 0
TCPC_UPC.TCCE
TCPC_UPS.CC2RDT
450 µs glitch filter
560 µs max delay
CC2
TCPC_UPC.RDIPSEL == 0
TCPC_UPC.TCIDSEL == 1
&&
TCPC_UPC.TCCE
TCPC_UPC.RDSEL
RdOTP
TCPC_UPC.RDCAL
0
0
1
2
3
Rd
TCPC_UPC.RDSRC