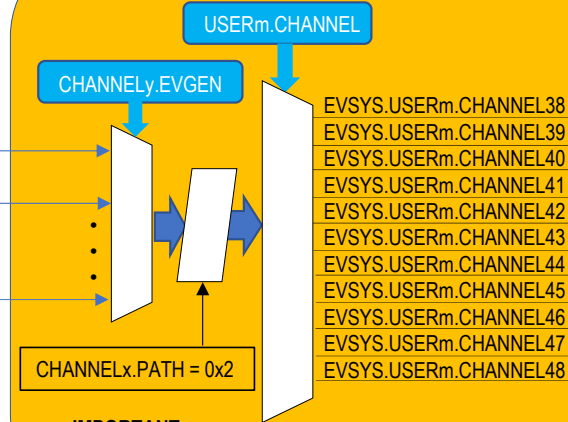


(EVSYS) Event System Module



IMPORTANT:

MUST use CHANNELx.PATH = 0x2 Asynchronous Path for deterministic ADC trigger timing for sample/Convert

ADC Module

