GLKGEN[0] GLKGEN[1] GLKGEN[2] Clock → GCLK PERIPHm The number of GCLK Gate inputs is device The maximum number of specific with n Generators and Peripherals GLKGEN[n] PCHCTRLm.CHEN representing the is device specific and is maximum possilbe. represented by m. Each Generator can be allocated PCHCTRLm.GEN to one or more Peripherals.