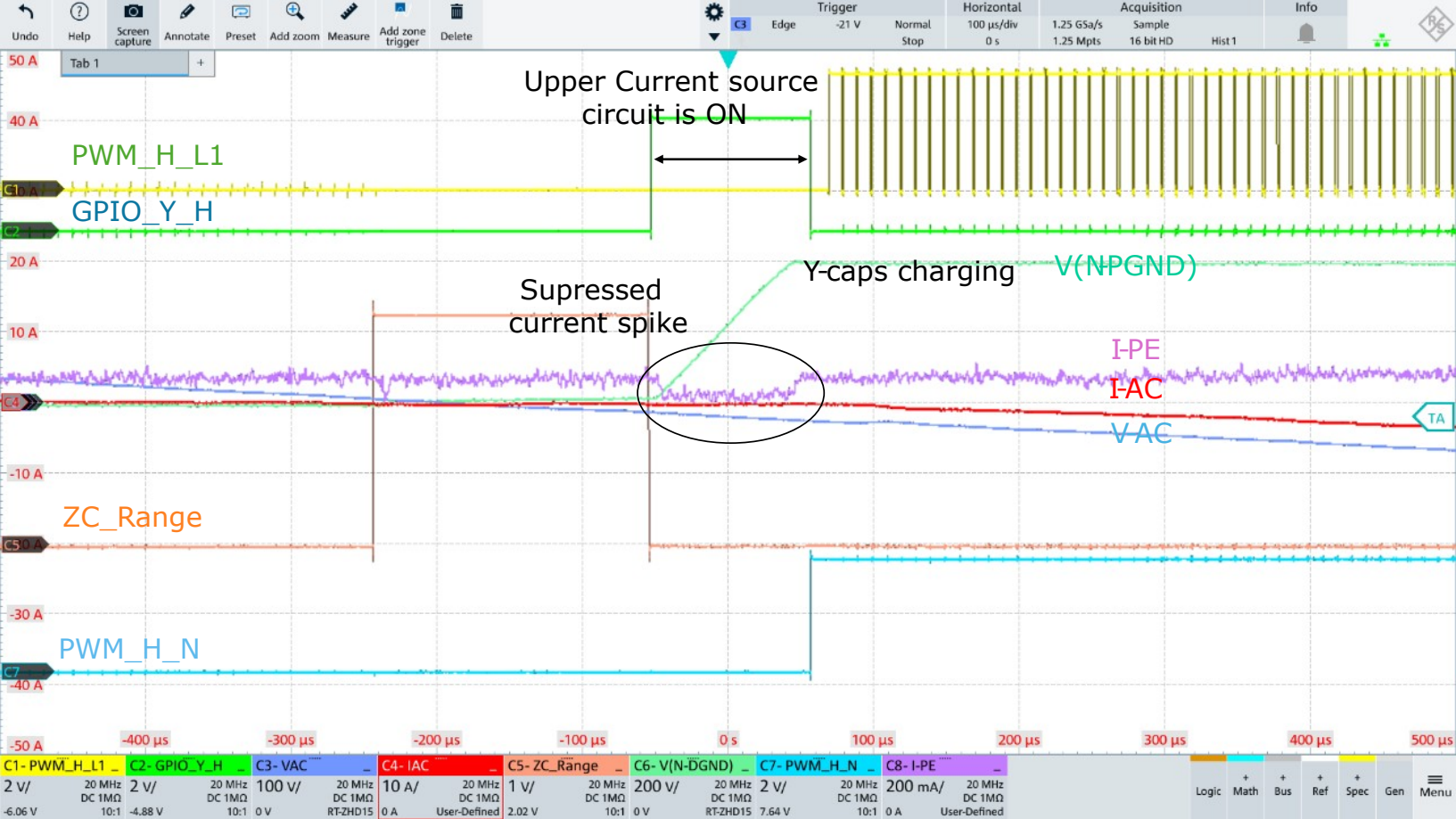
Undo
Help
Screen capture
Annotate
Preset
Add zoom
Measure
Add zone trigger
Delete
Trigger
C3 Edge -21 V Normal Stop
Horizontal
100 µs/div 0 s
Acquisition
1.25 GSa/s 1.25 Mpts Sample 16 bit HD Hist 1
Info
Tab 1
Upper Current source circuit is ON
PWM_H_L1
GPIO_Y_H
Y-caps charging
V(N-PGND)
Supressed current spike
I-PE
I-AC
V-AC
ZC_Range
PWM_H_N
50 A
40 A
20 A
10 A
-10 A
-30 A
-40 A
-50 A
-400 µs
-300 µs
-200 µs
-100 µs
0 s
100 µs
200 µs
300 µs
400 µs
500 µs
C1- PWM_H_L1 2 V/ 20 MHz DC 1MΩ 10:1 -6.06 V
C2- GPIO_Y_H 2 V/ 20 MHz DC 1MΩ 10:1 -4.88 V
C3- VAC 100 V/ 20 MHz DC 1MΩ RT-ZHD15 0 V
C4- IAC 10 A/ 20 MHz DC 1MΩ User-Defined 0 A
C5- ZC_Range 1 V/ 20 MHz DC 1MΩ 10:1 2.02 V
C6- V(N-DGND) 200 V/ 20 MHz DC 1MΩ RT-ZHD15 0 V
C7- PWM_H_N 2 V/ 20 MHz DC 1MΩ 10:1 7.64 V
C8- I-PE 200 mA/ 20 MHz DC 1MΩ User-Defined 0 A
Logic
Math
Bus
Ref
Spec
Gen
Menu