Bits 5:3 – CLKSEL[2:0] Clock Select

This bit field controls the Timer/Counter clock source and cannot be changed while the Timer/Counter is enabled.

Value	Name	Description
0x0	CLKPER	Peripheral clock
0x1	EVENT	Event edge
0x2	OSCHF	Internal high-frequency oscillator
0x3	OSC32K	Internal 32.768 kHz oscillator
0x4	XOSCHF	High-frequency crystal oscillator
0x5	PLL	Phase Locked Loop
Others	-	Reserved