Bit	7	6	5	4	3	2	1	0
	CMP1EV	CMP0EV		CLKSEL[2:0]			WGMODE[2:0]	
Access	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W
Reset	0	0	0	0	0	0	0	0