

Kit Window x Start Page x **Project Graph** x

✖ [Icons] Plugins: [v] Profiles: Main [v] View: Root [v]

The Project Graph displays several components in a hierarchical structure. On the left, there are three boxes: 'EVSYS Peripheral Library', 'NVMCTRL Peripheral Library' with 'MEMORY' highlighted in bold, and 'Device Family Pack (DFP)'. In the center, there is a 'CMSIS Pack' box. On the right, there is a 'System' box and a box for 'ADC0 Peripheral Library' with 'ADC' highlighted in bold. The 'ADC0' box is highlighted with a red rectangle.

Configuration Options x

[-] [+]

ADC0

- Select Prescaler: Peripheral clock divided by 8 [v]
- Select Sample Length (cycles): 4 [v]
- **** Conversion Time is 2.66666666667 us ****
- Select Reference: AVDD [v]
- Select Conversion Trigger: HW Event Trigger [v]
- Flush Event Input: Disabled [v]
- Start Event Input: Enabled on Rising Edge [v]
- Automatic Sequence Configuration
 - Enable Automatic Sequencing ☒
 - Enable ADC AIN0 Pin ☒
 - Enable ADC AIN1 Pin ☐
 - Enable ADC AIN2 Pin ☒
 - Enable ADC AIN3 Pin ☐
 - Enable ADC AIN4 Pin ☒
 - Enable ADC AIN5 Pin ☐