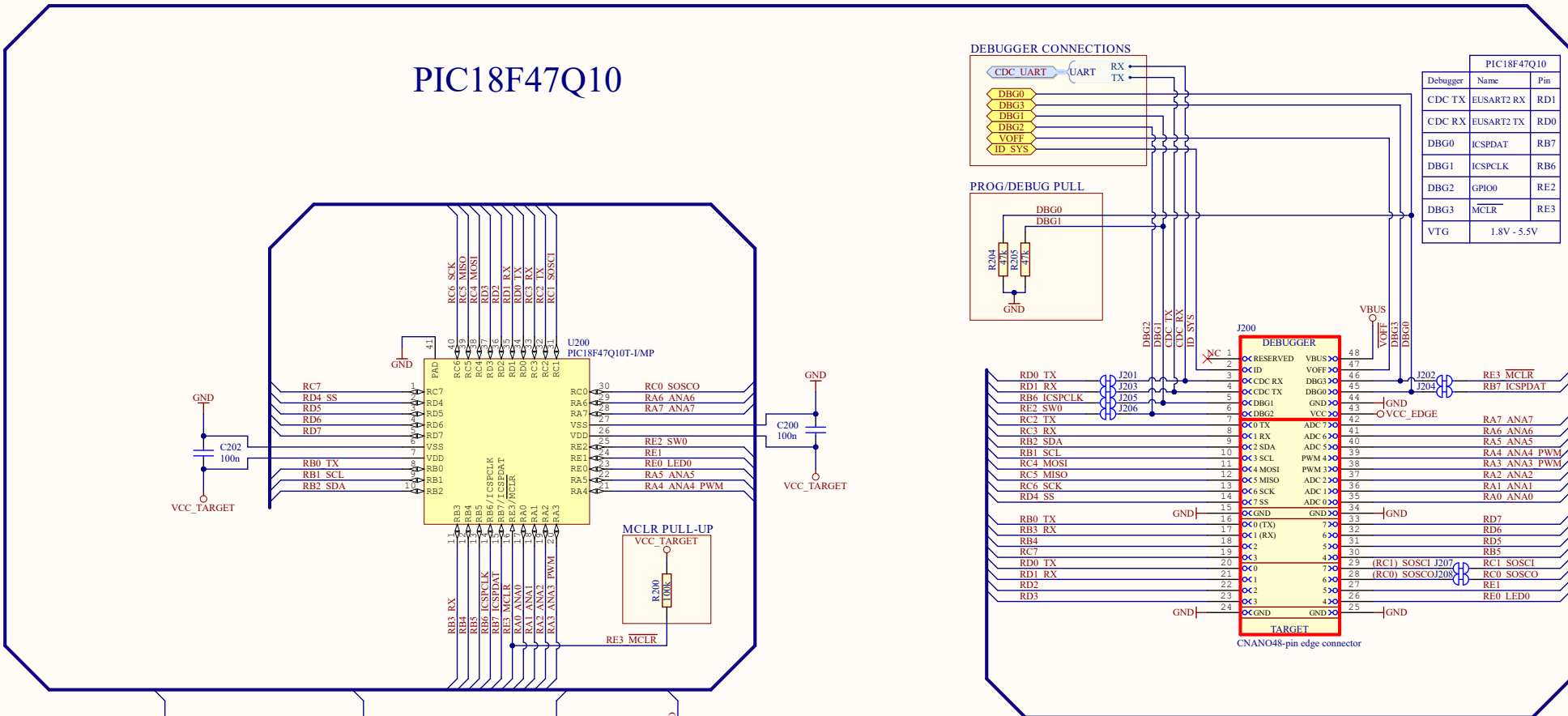
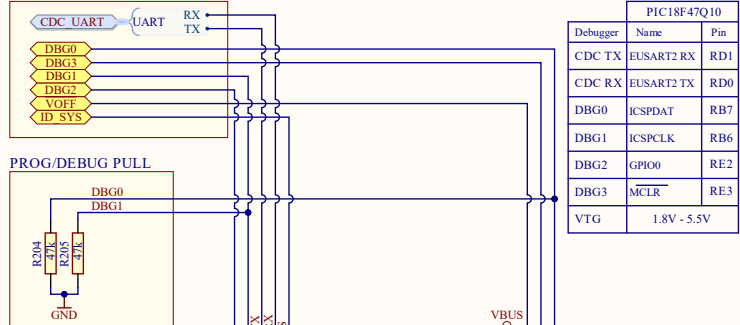


PIC18F47Q10



PIC18F47Q10		
Debugger	Name	Pin
CDC TX	EUSART2 RX	RD1
CDC RX	EUSART2 TX	RD0
DBG0	ICSPDAT	RB7
DBG1	ICSPCLK	RB6
DBG2	GPI00	RE2
DBG3	MCLR	RE3
VTG		1.8V - 5.5V

DEBUGGER CONNECTIONS



PROG/DEBUG PULL



NOTE on UART/CDC:
RX/TX on the header denotes the input/output direction of the signal respective to it's source.

CDC TX is output from the on-board debugger. CDC RX is input to the on-board debugger. TX is output from the TARGET device. RX is input to the TARGET device.

NOTE on I2C:
No pull-ups on board. Pull-ups should be mounted close to I2C clients(s).

NOTE on Crystal calculations:
The calculations are based on a crystal used on an earlier revision of this board.

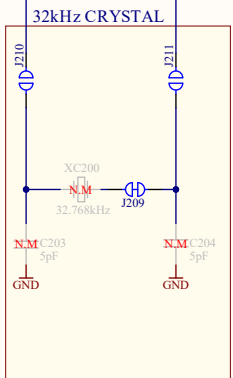
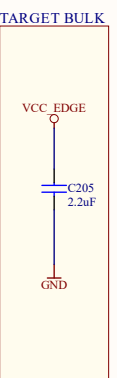
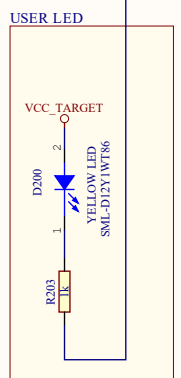
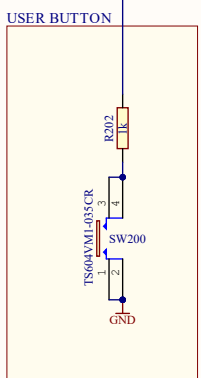
Crystal datasheet:
Crystal = 7pF
max ESR = 70kOhm
Accuracy ±20ppm

PIC18F47Q10 datasheet:
C_{xin} = 5pF (estimate)
C_{xout} = 5pF (estimate)
Maximum Load = ?
Maximum ESR = ?

Estimated C_{pcb} = 2pF

Estimated load
C = 2 (Crystal- C_{para} - C_{pcb})
C = 2 (7pF - 2.5pF - 2pF)
C = 5pF

Selected in design
C = 5pF



Drawn By: ML
Engineer: TF

Project Title: PIC18F47Q10 Curiosity Nano
Sheet Title: Target MCU

Size: A3 | PCB Assembly Number: A09-3246 | PCB Number: A08-2972
PCB Revision: 5 | PCB Revision: 3

File: PIC18F47Q10_Curiosity_Nano_Target_MCU.SchDoc

Designed with: **Altium**
Altium.com

Date: 8/9/2022
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