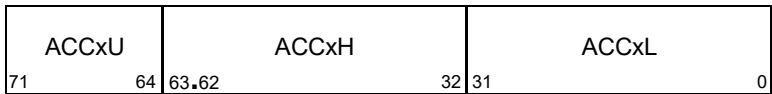


A)



Implied Radix Point (between bits 63 and 62)

B)



C)



D)



A) 72-bit accumulator consists of ACCxU:ACCxH:ACCxL

B) Load and store operations

C) Operations in Normal Saturation mode

D) Operations in Super Saturation mode or with saturation disabled