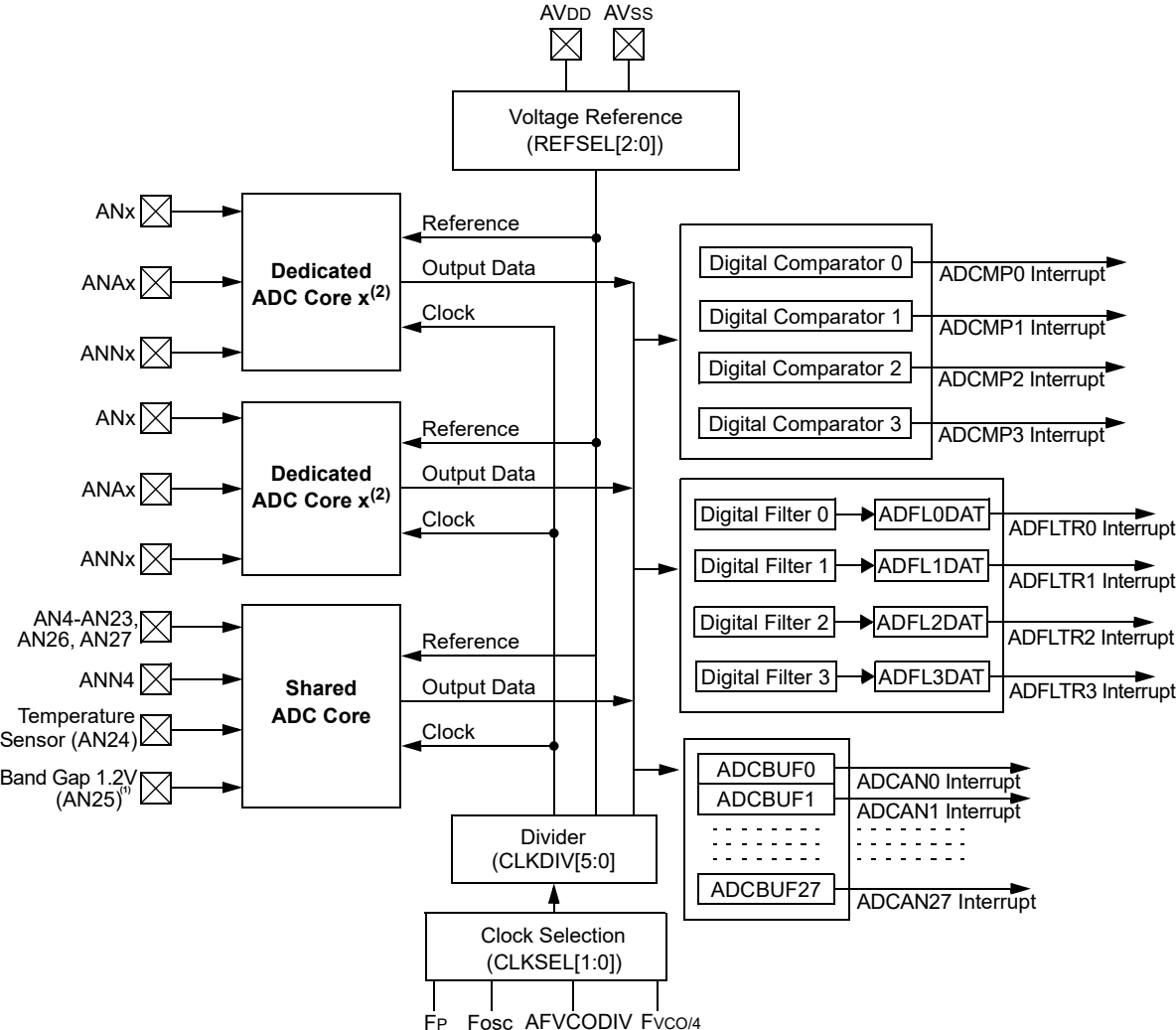
AVDD
AVSS
Voltage Reference
(REFSEL[2:0])
ANx
ANAx
ANNx
Dedicated
ADC Core x(2)
Reference
Output Data
Clock
ANx
ANAx
ANNx
Dedicated
ADC Core x(2)
Reference
Output Data
Clock
AN4-AN23,
AN26, AN27
ANN4
Temperature
Sensor (AN24)
Band Gap 1.2V
(AN25)(1)
Shared
ADC Core
Reference
Output Data
Clock
Digital Comparator 0
Digital Comparator 1
Digital Comparator 2
Digital Comparator 3
ADCMP0 Interrupt
ADCMP1 Interrupt
ADCMP2 Interrupt
ADCMP3 Interrupt
Digital Filter 0
Digital Filter 1
Digital Filter 2
Digital Filter 3
ADFL0DAT
ADFL1DAT
ADFL2DAT
ADFL3DAT
ADFLTR0 Interrupt
ADFLTR1 Interrupt
ADFLTR2 Interrupt
ADFLTR3 Interrupt
ADCBUF0
ADCBUF1
ADCBUF27
ADCAN0 Interrupt
ADCAN1 Interrupt
ADCAN27 Interrupt
Divider
(CLKDIV[5:0])
Clock Selection
(CLKSEL[1:0])
FP
Fosc
AFVCODIV
FVCO/4