



ΔT	150 ns	N_{falling}	1
N_{rising}	0	f_{min}	N/A
f_{max}	N/A	f_{mean}	N/A
T_{std}	N/A		

Low-side dead-time - The PWM signal from TCE's CMP0 and its complementary signal generated using WEX are not overlapping, due to the added 150 ns dead time

