Bit	7	6	5	4	3	2	1	0
				FDFEVC	FDFEVB	FDFEVA		FAULTDET
Access				R/W	R/W	R/W		R/W
Reset				0	0	0		0

Bit 4 – FDFEVC Fault Detection Flag Event Input C

This bit is set on a fault detection on Event input C. The bit it cleared by writing a '1' to its bit location.

Bit 3 – FDFEVB Fault Detection Flag Event Input B

This bit is set on a fault detection on Event input B. The bit it cleared by writing a '1' to its bit location.

Bit 2 – FDFEVA Fault Detection Flag Event Input A

This bit is set on a fault detection on Event input A. The bit it cleared by writing a '1' to its bit location.

Bit 0 – FAULTDET Fault Detection Interrupt Flag

This bit is set on a positive edge of fault detection. The bit it cleared by writing a '1' to its bit location.