Bit	7	6	5	4	3	2	1	0
					TCE0[3:0]			
Access					R/W	R/W	R/W	R/W
Reset					0	0	0	0

Bits 3:0 – TCE0[3:0] TCE0 Signals

This bit field controls the pin positions for TCE0 signals.

Value	Name	Description							
		WO0	WO1	WO2	WO3	WO4	WO5	WO6	WO7
0x0	PORTA	PA0	PA1	PA2	PA3	PA4	PA5	PA6	PA7
0x1	-	Reserved							
0x2	PORTC	PC0	PC1	PC2	PC3	-	-	-	-
0x3	PORTD	PD0	PD1	PD2	PD3	PD4	PD5	PD6	PD7
0x4	-	Reserved							
0x5	PORTF	PF0	PF1	PF2	PF3	PF4	PF5	-	-
0x6 - 0x7	-	Reserved							
0x8	PORTC2	PA0	PA1	PC0	PC1	PC2	PC3	-	-
0x9	PORTA2	PA2	PA3	PA4	PA5	PA6	PA7	-	-
Others	-	Reserved							