

Bit	7	6	5	4	3	2	1	0
	PGMOVR7	PGMOVR6	PGMOVR5	PGMOVR4	PGMOVR3	PGMOVR2	PGMOVR1	PGMOVR0
Access	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W
Reset	0	0	0	0	0	0	0	0

#### Bits 0, 1, 2, 3, 4, 5, 6, 7 – PGMOVR Pattern Generation Override

This register holds override enable PGM mode. If a bit is set to '1' the corresponding bit in PGMOUT specifies the value that will override the output from the SWAP unit.

The available configuration for each bit n in this bit field is shown in the table below:

Value	Description
0	The pin n (Pxn) output is not overwritten
1	The pin n (Pxn) output is overwritten with the value in PGMOUT register