

**Bits 2:0 – WGMODE[2:0] Waveform Generation Mode**

This bit field selects the Waveform Generation mode and controls the counting sequence of the counter, TOP value, UPDATE condition, interrupt condition, and the type of waveform generated.

No waveform generation is performed in the Normal mode of operation. For all other modes, the waveform generator output will only be directed to the port pins if the corresponding CMPnEN bit has been set. The port pin direction must be set as output.

Value	Name	Description
0x0	NORMAL	Normal operation mode
0x1	FRQ	Frequency mode
0x2	-	Reserved
0x3	SINGLESLOPE	Single-slope PWM mode
0x4	-	Reserved
0x5	DSTOP	Dual-slope PWM mode with overflow on TOP
0x6	DSBOTH	Dual-slope PWM mode with overflow on TOP and BOTTOM
0x7	DSBOTTOM	Dual-slope PWM mode with overflow on BOTTOM