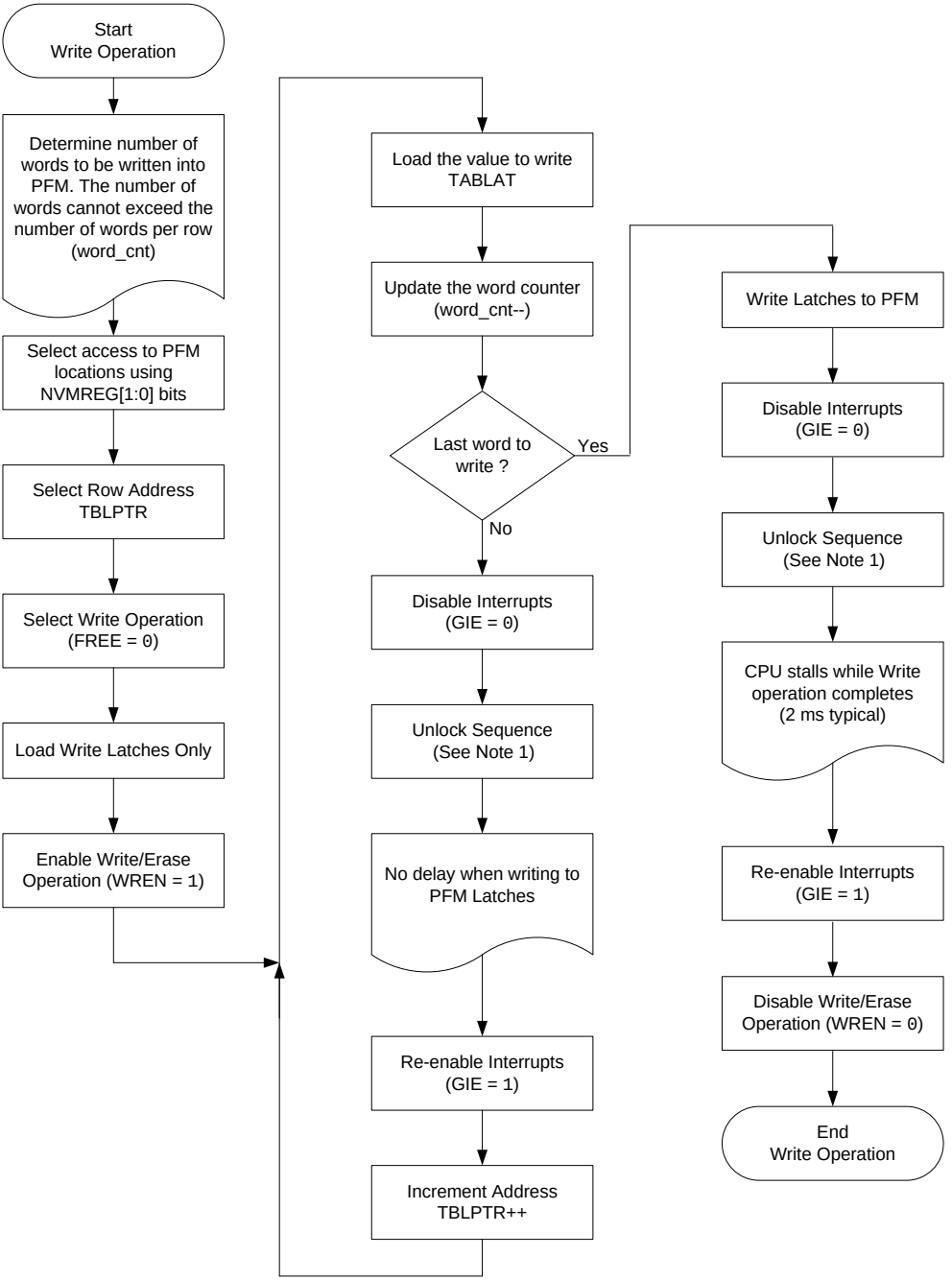
Start
Write Operation
Determine number of words to be written into PFM. The number of words cannot exceed the number of words per row (word_cnt)
Select access to PFM locations using NVMREG[1:0] bits
Select Row Address TBLPTR
Select Write Operation (FREE = 0)
Load Write Latches Only
Enable Write/Erase Operation (WREN = 1)
Load the value to write TABLAT
Update the word counter (word_cnt--)
Last word to write ?
Yes
No
Disable Interrupts (GIE = 0)
Unlock Sequence (See Note 1)
No delay when writing to PFM Latches
Re-enable Interrupts (GIE = 1)
Increment Address TBLPTR++
Write Latches to PFM
Disable Interrupts (GIE = 0)
Unlock Sequence (See Note 1)
CPU stalls while Write operation completes (2 ms typical)
Re-enable Interrupts (GIE = 1)
Disable Write/Erase Operation (WREN = 0)
End
Write Operation