

BANK 56		BANK 57		BANK 58		BANK 59		BANK 60		BANK 61		BANK 62		BANK 63					
1C00h	Core Registers	1C80h	Core Registers	1D00h	Core Registers	1D80h	Core Registers	1E00h	Core Registers	1E80h	Core Registers	1F00h	Core Registers	1F80h	Core Registers				
1C0Bh	Unimplemented Read as '0'	1C8Bh	Unimplemented Read as '0'	1D0Bh	Unimplemented Read as '0'	1D8Bh	Unimplemented Read as '0'	1E0Bh	See Table 2 for register mapping details	1E8Bh	See Table 3 for register mapping details	1F0Bh	Unimplemented Read as '0'	1F8Bh	Unimplemented Read as '0'				
1C0Ch		1C8Ch		NVMADRL		1D0Ch		ADLTHL		1D8Ch		RA0PPS		1E0Ch		1E8Ch	1F0Ch	UMTOAP	1F8Ch
		1C8Dh		NVMADRH		1D0Dh		ADLTHH		1D8Dh		RA1PPS					1F0Dh	UMTOAL	
		1C8Eh		NVMDATL		1D0Eh		ADUTHL		1D8Eh		RA2PPS					1F0Eh	UMTOAH	
		1C8Fh		NVMDATH		1D0Fh		ADUTHH		1D8Fh		RA3PPS					1F0Fh		
		1C90h		NVMCON1		1D10h		ADERRL		1D90h		RA4PPS							
		1C91h		NVMCON2		1D11h		ADERRH		1D91h		RA5PPS							
		1C92h				1D12h		ADSTPTL		1D92h		RA6PPS							
						1D13h		ADSTPTH		1D93h		RA7PPS							
						1D14h		ADFLTRL		1D94h		RB0PPS							
						1D15h		ADFLTRH		1D95h		RB1PPS							
						1D16h		ADACCL		1D96h		RB2PPS							
						1D17h		ADACCH		1D97h		RB3PPS							
						1D18h		ADACCU		1D98h		RB4PPS							
						1D19h		ADCNT		1D99h		RB5PPS							
						1D1Ah		ADRPT		1D9Ah		RB6PPS							
						1D1Bh		ADPREVL		1D9Bh		RB7PPS							
						1D1Ch		ADPREVH		1D9Ch		RC0PPS							
						1D1Dh		ADRESL		1D9Dh		RC1PPS							
						1D1Eh		ADRESH		1D9Eh		RC2PPS							
			1D1Fh	ADPCH	1D9Fh	RC3PPS													
			1D20h	ADNCH	1DA0h	RC4PPS													
			1D21h	ADACQL	1DA1h	RC5PPS													
			1D22h	ADACQH	1DA2h	RC6PPS													
			1D23h	ADCAP	1DA3h	RC7PPS													
			1D24h	ADPREL	1DA4h	RD0PPS ⁽¹⁾													
			1D25h	ADPREH	1DA5h	RD1PPS ⁽¹⁾													
			1D26h	ADCON0	1DA6h	RD2PPS ⁽¹⁾													
			1D27h	ADCON1	1DA7h	RD3PPS ⁽¹⁾													
			1D28h	ADCON2	1DA8h	RD4PPS ⁽¹⁾													
			1D29h	ADCON3	1DA9h	RD5PPS ⁽¹⁾													
			1D2Ah	ADSTAT	1DAAh	RD6PPS ⁽¹⁾													
			1D2Bh	ADREF	1DABh	RD7PPS ⁽¹⁾													
			1D2Ch	ADACT	1DACH	RE0PPS ⁽¹⁾													
			1DCDh	ADCLK	1DADh	RE1PPS ⁽¹⁾													
			1D2Eh	ADCG1A	1DAEh	RE2PPS ⁽¹⁾													
			1D2Fh	ADCG1B	1DAFh														
			1D30h	ADCG1C															
			1D31h	ADCG1D ⁽¹⁾															
			1D32h	ADCG1E ⁽¹⁾															
			1D33h	Unimplemented Read as '0'															
1C6Fh	Common RAM (Accesses 70h-7Fh)	1CEFh	Common RAM (Accesses 70h-7Fh)	1D6Fh	Common RAM (Accesses 70h-7Fh)	1DEFh	Common RAM (Accesses 70h-7Fh)	1E6Fh	Common RAM (Accesses 70h-7Fh)	1EEFh	Common RAM (Accesses 70h-7Fh)	1F6Fh	Common RAM (Accesses 70h-7Fh)	1FEFh	Common RAM (Accesses 70h-7Fh)				
1C70h		1CF0h		1D70h		1DF0h		1E70h		1EF0h		1F70h		1FF0h					
1C7Fh		1CFh		1D7Fh		1DFh		1E7Fh		1EFh		1F7Fh		1FFh					

Note: 1. 40-pin devices only.

Legend:

■ Unimplemented data memory locations, read as '0'