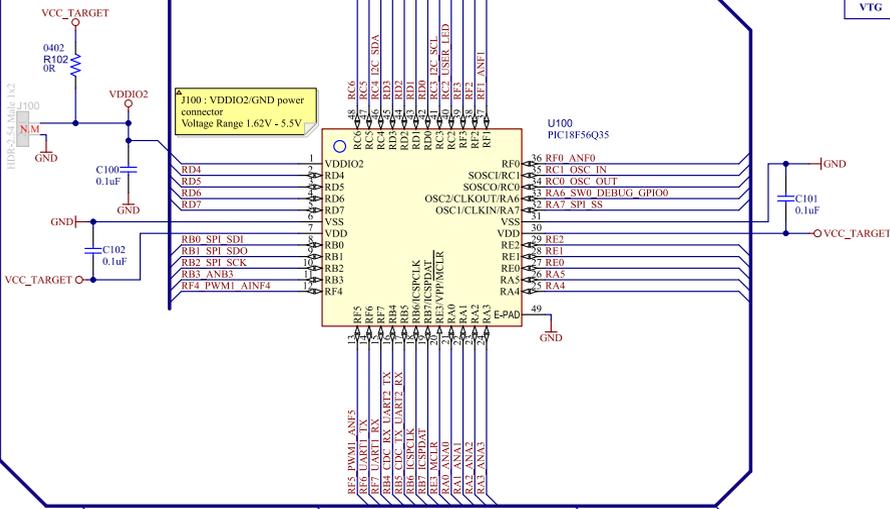
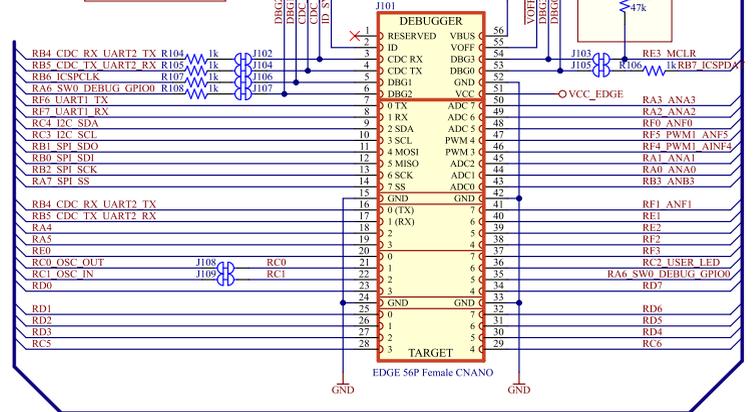
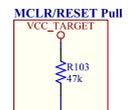
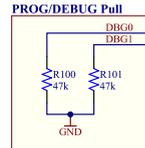


# PIC18F56Q35

**WARNING:** Remove R102 before applying power to J100

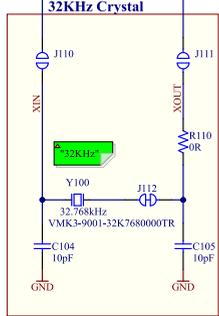
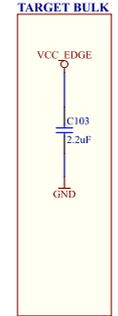
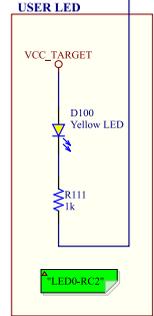
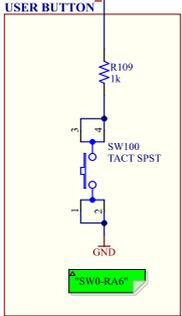


PIC18F56Q35		
Debugger	Name	Pin
CDC TX	USART2 RX	RB5
CDC RX	USART2 TX	RB4
DBG0	ICSPDAT	RB7
DBG1	ICSPCLK	RB6
DBG2	GPIO0	RA6
DBG3	MCLR	RE3
VTG	1.8V - 5.5V	



**NOTE on UART/CDC:**  
RX/TX on the header denotes the input/output direction of the signal respective to it's source.  
  
CDC TX is output from the DEBUGGER.  
CDC RX is input to the DEBUGGER.  
TX is output from the TARGET device.  
RX is input to the TARGET device.

**NOTE on I2C:**  
No pull-ups on the header. Pull-ups must be mounted close to client device(s).



Crystal datasheet:  
Crystal = 9pF  
max ESR = 70kOhm  
Accuracy ±20ppm  
  
Q35 datasheet:  
Cxin = 5pF (typical)  
Cxout = 5pF (typical)  
Cl = 1 (1.5pF) ± (1.5pF) = 2.5pF  
Maximum Load = 15pF (High Power mode)  
Maximum ESR = 100kOhm (High Power mode)  
  
Estimated Cpcb = 1pF  
  
Estimated load  
C = 2 (Crystal-Cpara - Cpcb)  
C = 2 (9pF - 2.5pF - 1pF)  
C = 11pF  
  
Designed in design after validation  
C = 10/10pF  
Designed and validated for High Power mode

Project Owner: **SBM**

PCB Layout Contact: **SBM**

PartNumber: **EVS5P36A** Project Title: **PIC18F56Q35 Curiosity Nano**

Sheet Title: **PIC18F56Q35 Curiosity Nano** Variant: **Default Assembly**

Target MCU

Size: **A3** SCH #: **02-01592** Rev: **2** Date: **08.01.2026**

PCB #: **04-12631** Rev: **2** Sheet: **2 of 4**

Designed with **Altium**

File: **PIC18F56Q35 Curiosity Nano Target MCU.SchDoc**