## AT27C512R: Package Marking Information

28-Lead PDIP		32-Lead PLCC	
Topside	Backside	Topside	Backside
ATMEL AT27C512R %%U-34A04B YYWWNNN		ATMEL AT27C512R %%U-34A04B YYWWNNN	
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## Note: no backside markings

	%% = Access Time
	45: 45 ns 70: 70 ns
	Lot Trace Code
Y	WWNNN: Lot Trace Code ′: Year, WW: Work Week INN = Assembly Trace Code