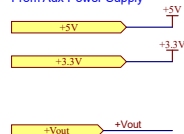
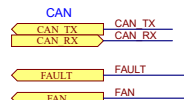
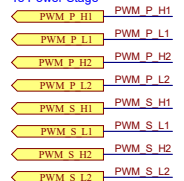


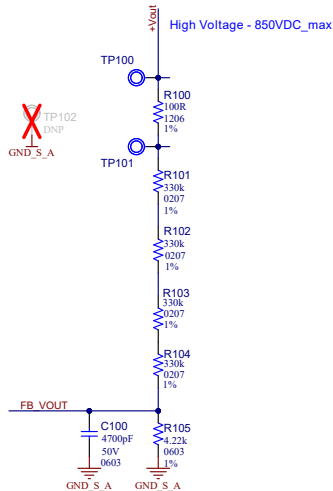
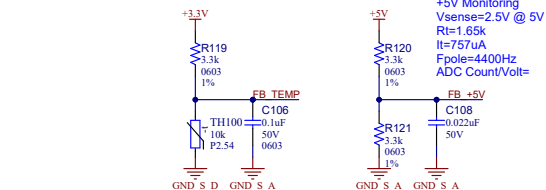
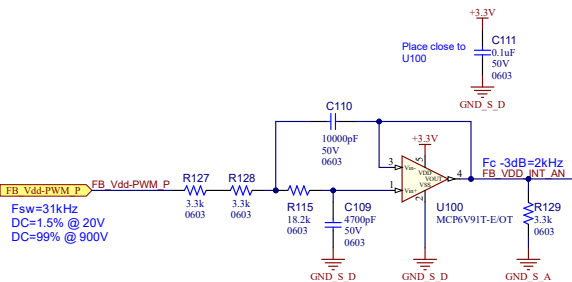
From Aux Power Supply



To Power Stage

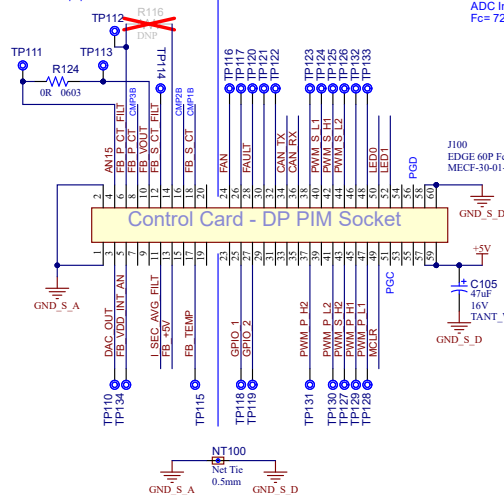


Open Loop
Fsw=20kHz
Dmin=45%
Dmax=100%



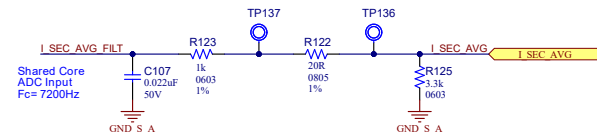
HV:
FB_Vout=3.18mV/V
Vsense=2.549V @ 800V
Rt=4.2k
It=623uA
Fpole=8kHz
ADC Count/Volt=4

populate R116 for CH PIM

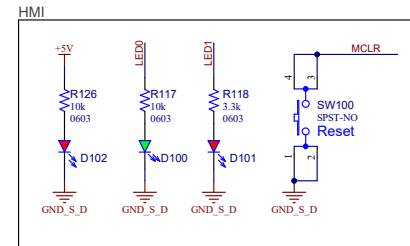


Use microUSB port
for development purpose
and UART communication.

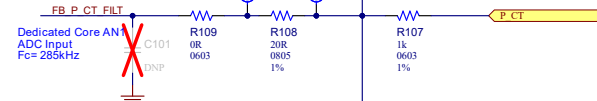
CT sense=25mV/A
Voffset= 1.65V



Shared Core
ADC Input
Fc= 720kHz



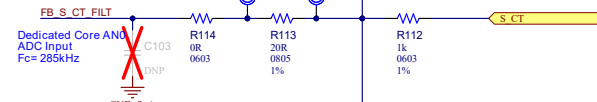
CT sense=82.5mV/A
Voffset= 500mV



CT sense=66mV/A
Voffset= 500mV



CT sense=66mV/A
Voffset= 500mV



CT sense=66mV/A
Voffset= 500mV

