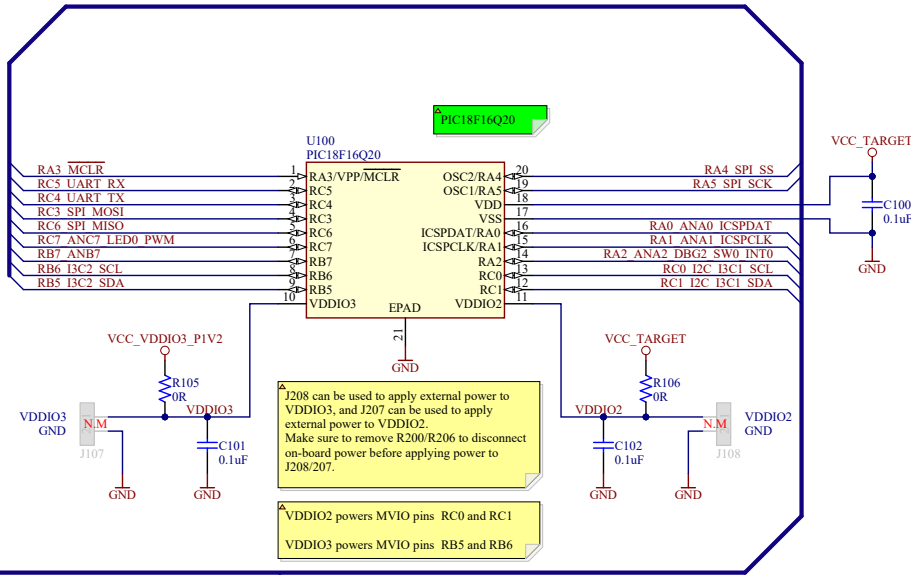


PIC18F16Q20

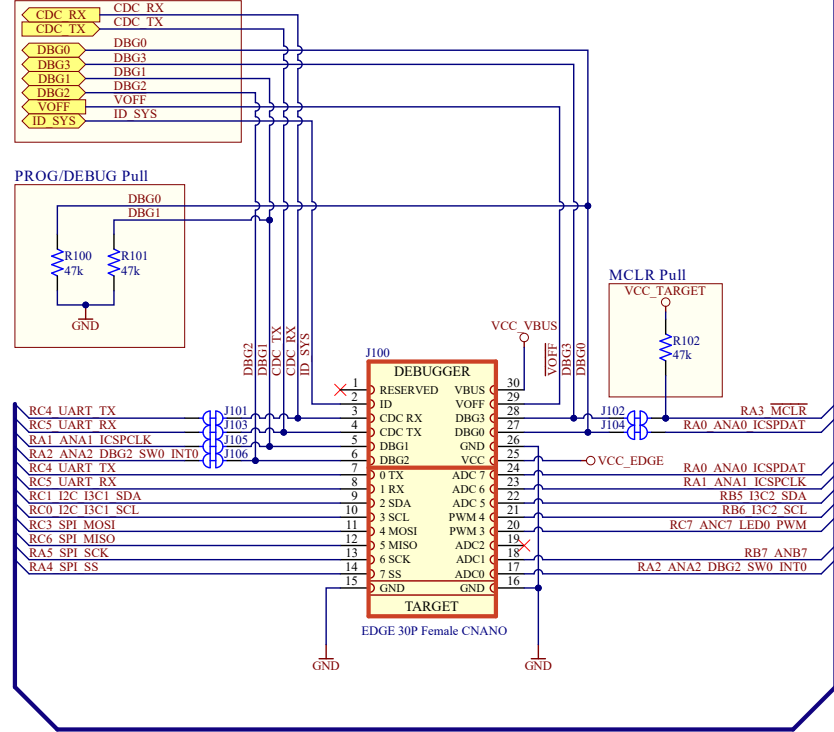
PIC18F16Q20		
Debugger	Name	Pin
CDC TX	UART[n] RX	RC5
CDC RX	UART[n] TX	RC4
DBG0	ICSPDAT	RA0
DBG1	ICSPCLK	RA1
DBG2	SW0 / GPIO0	RA2
DBG3	MCLR	RA3
VTG	1.8V - 3.6V	



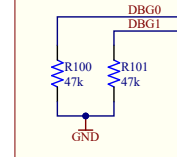
J208 can be used to apply external power to VDDIO3, and J207 can be used to apply external power to VDDIO2. Make sure to remove R200/R206 to disconnect on-board power before applying power to J208/207.

VDDIO2 powers MVIO pins RC0 and RC1
VDDIO3 powers MVIO pins RB5 and RB6

DEBUGGER CONNECTIONS



PROG/DEBUG Pull

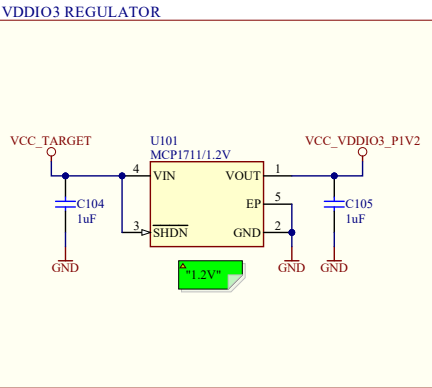
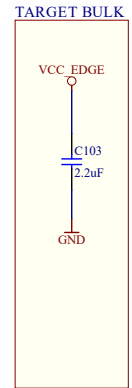
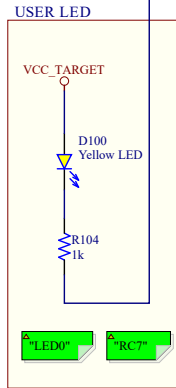
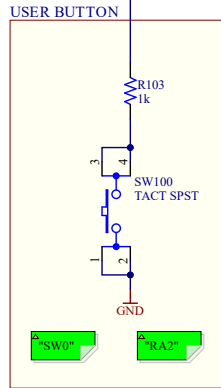


MCLR Pull



NOTE on I2C:
No pull-ups on board. Pull-ups should be mounted close to client device(s).

NOTE on UART/CDC:
RX/TX on the header denotes the input/output direction of the signal respective to it's source.
CDC TX is output from the DEBUGGER.
CDC RX is input to the DEBUGGER.
TX is output from the TARGET device.
RX is input to the TARGET device.



Project Owner: ST
PCB Layout Contact: TF

PartNumber: EV73T25A
Project Title: PIC18F16Q20 Curiosity Nano
Variant: Default Assembly

Sheet Title: Target MCU
Size: A3
SCH #: 02-00457
PCB #: 04-11563

Rev: 3
Date: 18.09.2023
Rev: 3
Sheet 2 of 4

Designed with Altium Altium.com

File: PIC18F16Q20_Curiosity_Nano_Target_MCU1.SchDoc