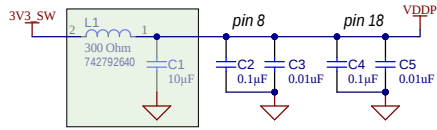
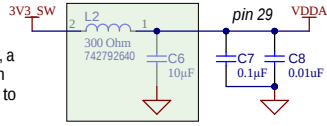


LAN8670 MII Application - With Wake/Sleep

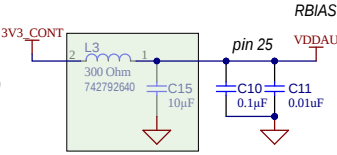
LAYOUT NOTE: Place one 0.01uF and one 0.1uF at each power pin. The 0.01uF must be closest to the pin.



Ferrite beads are optional. If used, a bulk capacitor is recommended on the device side of the ferrite bead to dampen potential oscillation.

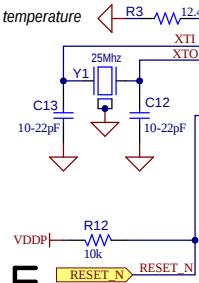


VDDAU:
Continuous supply for wake/sleep



NOTE: If the system reset is unused, RESET_N pin can be connected directly to VDDP

from system reset



INH / WAKE_IN: See Notes

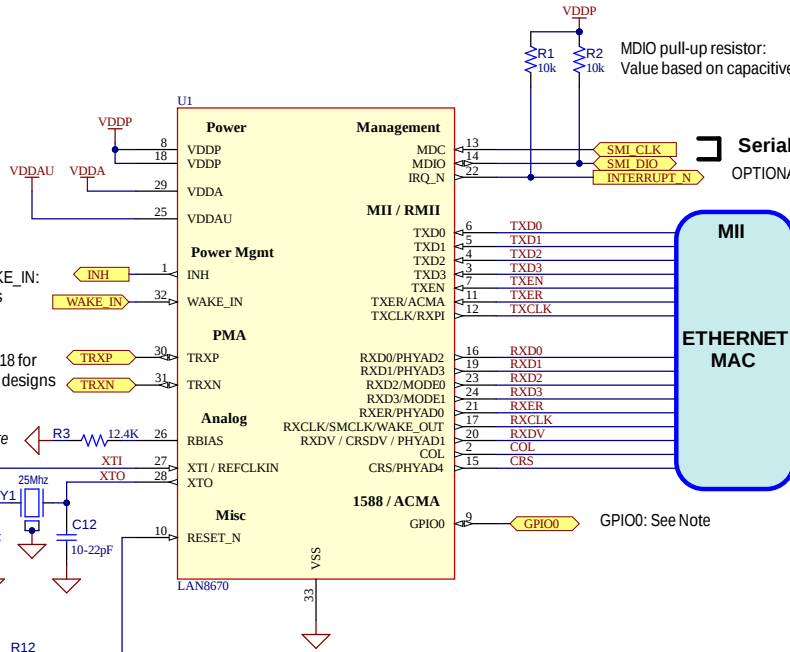
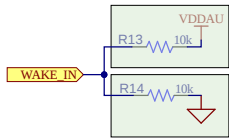
WAKE_IN

TRXP

TRXN

See AN1718 for latest BIN designs

WAKE_IN: polarity configured pull-up or pulldown (install R13 or R14, never both) (if unused, connect to VSS)

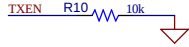
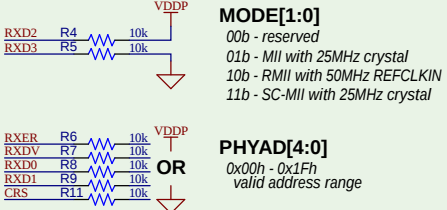


Hardware Configuration Straps:

Values are latched upon POR RESET_N negated.

EXTERNAL RESISTORS (REQUIRED)

10k Ohm typical Strap resistors.
Strap resistor value is dependent on the Ethernet MAC internal resistor values.
Strap resistors must be able to override the Ethernet MAC internal pull-up or down resistor to set a logic low or high.



NOTE: When the MAC sublayer is in reset or unconfigured, the TXEN output pin may become high impedance and floating. A pull-down can be added to prevent false TXEN assertions and accidental transmissions.

GPIO0: Configurable as ACMA, TXPI, RXPI or RXTXPI (if unused, leave unconnected)