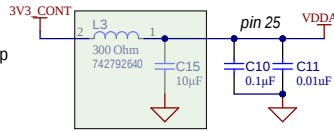
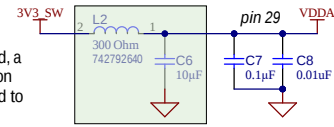
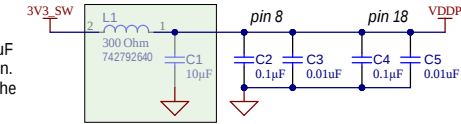


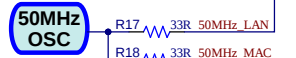
LAYOUT NOTE: Place one 0.01uF and one 0.1uF at each power pin. The 0.01uF must be closest to the pin.

Ferrite beads are optional. If used, a bulk capacitor is recommended on the device side of the ferrite bead to dampen potential oscillation.

VDDAU:
Continuous supply for wake/sleep

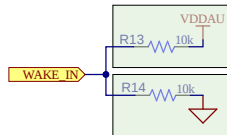


LAYOUT NOTE: 50MHz_LAN and 50MHz_MAC PCB traces should be length matched.

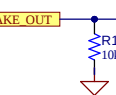


NOTE: If the system reset is unused, RESET_N pin can be connected directly to VDDP

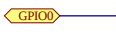
from system reset



WAKE_IN: polarity configured pull-up or pulldown (install R13 or R14, never both) (if unused, connect to VSS)



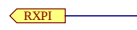
WAKE_OUT: asserted when part wakes (if unused, leave unconnected)



GPIO0: Configurable as TXPI or RXTXPI (if unused, leave unconnected)

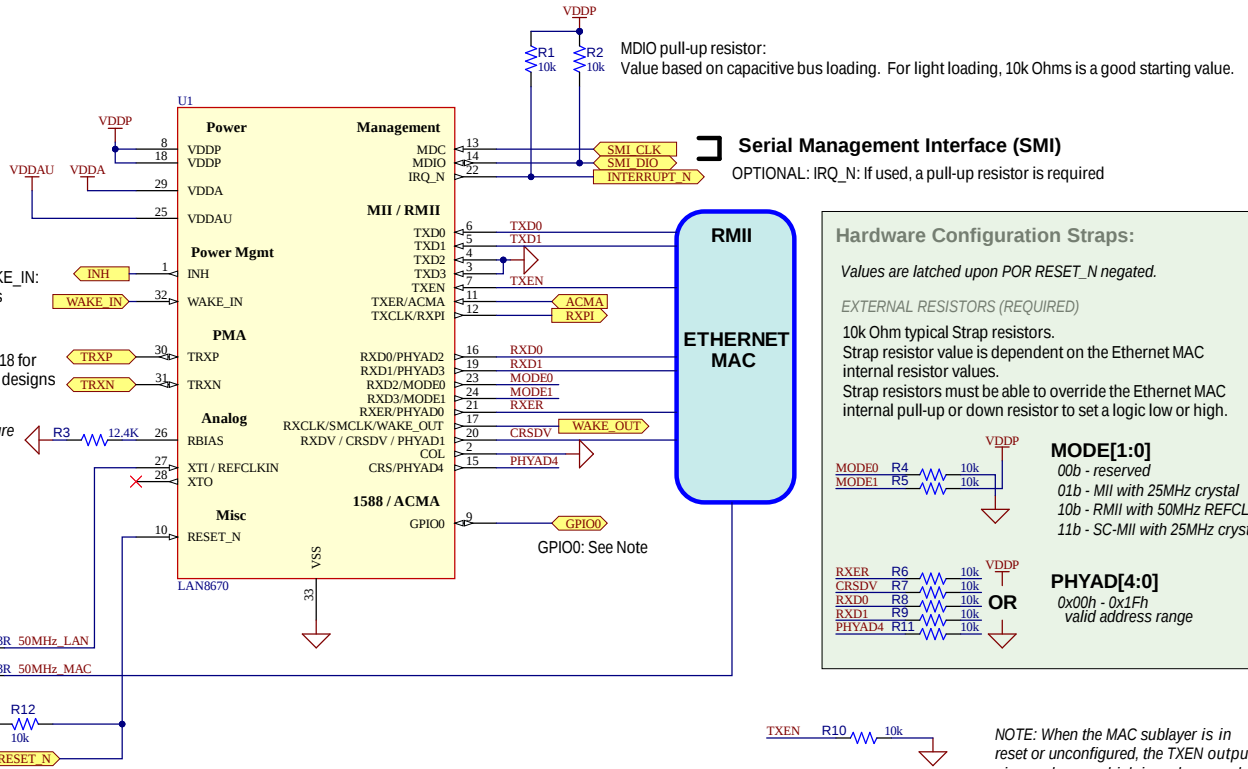


ACMA: Allows PHY to transmit to the medium. (if unused, connect to VSS)



RXPI: Asserted when PHY receives a packet. (if unused, leave unconnected)

LAN8670 RMII Application - With Wake/Sleep



from system reset