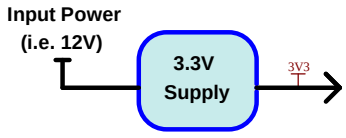
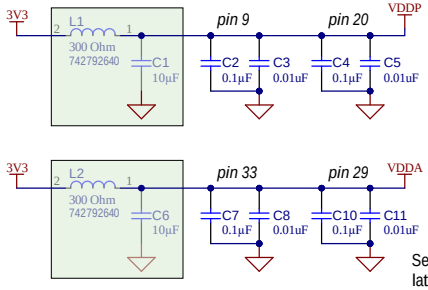
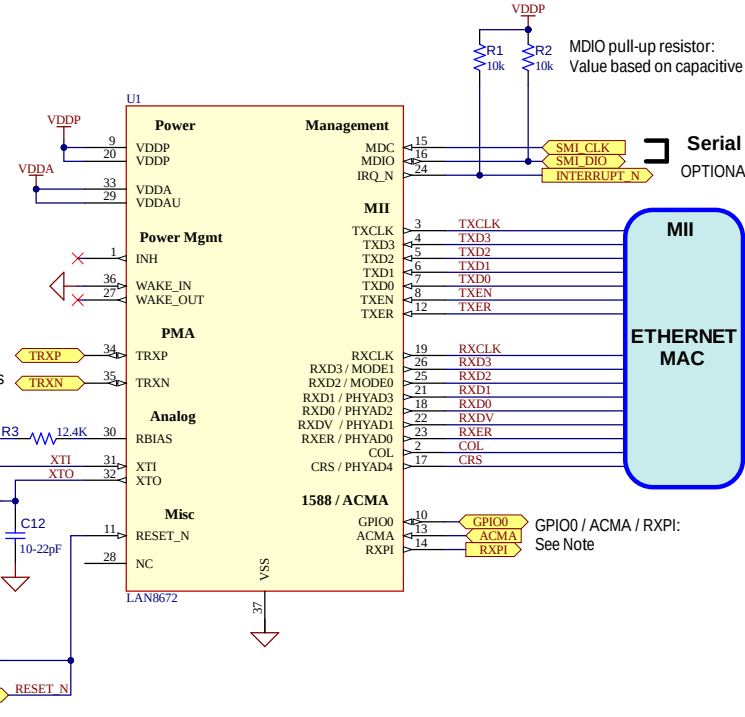
LAN8672 MII Application - No Wake/Sleep

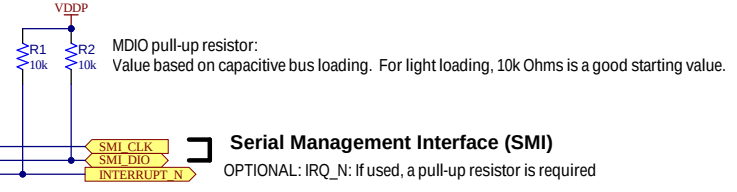


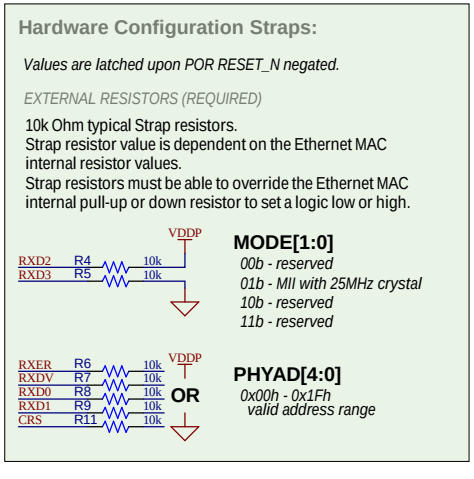
LAYOUT NOTE: Place one 0.01uF and one 0.1uF at each power pin. The 0.01uF must be closest to the pin.

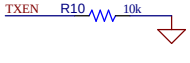
Ferrite beads are optional. If used, a bulk capacitor is recommended on the device side of the ferrite bead to dampen potential oscillation.











NOTE: When the MAC sublayer is in reset or unconfigured, the TXEN output pin may become high impedance and floating. A pull-down can be added to prevent false TXEN assertions and accidental transmissions.

ACMA: Allows PHY to transmit to the medium.
(if unused, connect to VSS)

GPIO0: Configurable as TXPI or RXTXPI
(if unused, leave unconnected)

RXPI: Asserted when PHY receives a packet.
(if unused, leave unconnected)