



Pin	SD	SPI
1	CD/DAT3	CS
2	CMD	DI
3	VSS1	VSS1
4	VDD	VDD
5	CLK	SCLK
6	VSS2	VSS2
7	DAT0	DO
8	DAT1	X
9	DAT2	X

Pin	SD	SPI
1	DAT2	X
2	CD/DAT3	CS
3	CMD	DI
4	VDD	VDD
5	CLK	SCLK
6	VSS	VSS
7	DAT0	DO
8	DAT1	X

Pin #	SD Mode	SPI Mode
	Name	Name
1	CD/DAT3 <sup>2</sup>	CS
2	CMD	DI
3	V <sub>SS1</sub>	V <sub>SS</sub>
4	V <sub>DD</sub>	V <sub>DD</sub>
5	CLK	SCLK
6	V <sub>SS2</sub>	V <sub>SS2</sub>
7	DAT0	DO
8	DAT1	RSV
9	DAT2	RSV
10	NC <sup>4</sup>	NC
11	NC <sup>4</sup>	NC