


BANK 8		BANK 9		BANK 10		BANK 11		BANK 12		BANK 13		BANK 14		BANK 15	
400h	Core Registers	480h	Core Registers	500h	Core Registers	580h	Core Registers	600h	Core Registers	680h	Core Registers	700h	Core Registers	780h	Core Registers
40Bh		48Bh		50Bh		58Bh		60Bh		68Bh		70Bh		78Bh	
40Ch	CCPR1L	48Ch	—	50Ch	CLBICON	58Ch	—	60Ch	—	68Ch	CLCnCON	70Ch	RC1REG	78Ch	SSP1BUF
40Dh	CCPR1H	48Dh	—	50Dh	CLBINU	58Dh	—	60Dh	—	68Dh	CLCnPOL	70Dh	TX1REG	78Dh	SSP1ADD
40Eh	CCP1CON	48Eh	—	50Eh	CLBINH	58Eh	—	60Eh	—	68Eh	CLCnSELO	70Eh	SP1GRBL	78Eh	SSP1MSK
40Fh	CCP1CAP	48Fh	—	50Fh	CLBINM	58Fh	—	60Fh	—	68Fh	CLCnSEL1	70Fh	SP1BRGH	78Fh	SSP1STAT
410h	CCPR2L	490h	—	510h	CLBINL	590h	—	610h	—	690h	CLCnSEL2	710h	RC1STA	790h	SSP1CON1
411h	CCPR2H	491h	—	511h	CLBOUTU	591h	—	611h	—	691h	CLCnSEL3	711h	TX1STA	791h	SSP1CON2
412h	CCP2CON	492h	—	512h	CLBOUTH	592h	—	612h	—	692h	CLCnGLS0	712h	BAUD1CON	792h	SSP1CON3
413h	CCP2CAP	493h	—	513h	CLBOUTM	593h	—	613h	—	693h	CLCnGLS1	713h	—	793h	—
414h	—	494h	—	514h	CLBOUTL	594h	—	614h	—	694h	CLCnGLS2	714h	—	794h	—
415h	—	495h	—	515h	CLBCLK	595h	—	615h	—	695h	CLCnGLS3	715h	—	795h	—
416h	—	496h	—	516h	CLBOEU	596h	—	616h	—	696h	CLCSELECT	716h	—	796h	—
417h	—	497h	—	517h	CLBOEH	597h	—	617h	—	697h	CLCDATA	717h	—	797h	—
418h	—	498h	—	518h	CLBOEM	598h	—	618h	—	698h	—	718h	—	798h	—
419h	—	499h	—	519h	CLBOEL	599h	—	619h	—	699h	—	719h	—	799h	—
41Ah	—	49Ah	—	51Ah	—	59Ah	—	61Ah	—	69Ah	—	71Ah	—	79Ah	—
41Bh	—	49Bh	—	51Bh	—	59Bh	—	61Bh	—	69Bh	—	71Bh	—	79Bh	—
41Ch	—	49Ch	—	51Ch	—	59Ch	—	61Ch	—	69Ch	—	71Ch	—	79Ch	—
41Dh	—	49Dh	—	51Dh	—	59Dh	—	61Dh	—	69Dh	—	71Dh	—	79Dh	—
41Eh	—	49Eh	—	51Eh	—	59Eh	—	61Eh	—	69Eh	—	71Eh	—	79Eh	—
41Fh	CCPTMR50	49Fh	—	51Fh	—	59Fh	—	61Fh	—	69Fh	—	71Fh	—	79Fh	—
420h	General Purpose Registers 80 Bytes <sup>(1)</sup>	4A0h	General Purpose Registers 80 Bytes <sup>(1)</sup>	520h	General Purpose Registers 80 Bytes <sup>(1)</sup>	5A0h	General Purpose Registers 80 Bytes <sup>(1)</sup>	620h	General Purpose Registers 48 Bytes <sup>(1)</sup>	6A0h	Unimplemented Read as '0'	720h	Unimplemented Read as '0'	7A0h	Unimplemented Read as '0'
								64Fh	Unimplemented Read as '0'						
								650h	Unimplemented Read as '0'						
								66Fh	Unimplemented Read as '0'						
470h	Common RAM (Accesses 70h-7Fh)	4F0h	Common RAM (Accesses 70h-7Fh)	570h	Common RAM (Accesses 70h-7Fh)	5F0h	Common RAM (Accesses 70h-7Fh)	670h	Common RAM (Accesses 70h-7Fh)	6F0h	Common RAM (Accesses 70h-7Fh)	770h	Common RAM (Accesses 70h-7Fh)	7F0h	Common RAM (Accesses 70h-7Fh)
47Fh		4FFh		57Fh		5FFh		67Fh		6FFh		77Fh		7FFh	

**Note:** 1. Available only on PIC16F131x5 devices

Legend:

 Unimplemented data memory locations, read as '0'