	BANK 16		BANK 17		BANK 18		BANK 19		BANK 20		BANK 21		BANK 22		BANK 23
800h	Core Registers	880h	Core Registers	900h	Core Registers	980h	Core Registers	A00h	Core Registers	A80h	Core Registers	B00h	Core Registers	B80h	Core Registers
80Bh		88Bh		90Bh		98Bh		A0Bh		A8Bh		B0Bh		B8Bh	
80Ch	CM1CON0	88Ch	DAC1CON0	90Ch	Unimplemented Read as '0'	98Ch	Unimplemented Read as '0'	A0Ch	Unimplemented Read as '0'	A8Ch	Unimplemented Read as '0'	B0Ch	Unimplemented Read as '0'	B8Ch	Unimplemented Read as '0'
80Dh	CM1CON1	88Dh	DAC1DATL											
80Eh	CM1NCH	88Eh	Unimplemented Read as '0'											
80Fh	CM1PCH													
810h	CM2CON0													
811h	CM2CON1													
812h	CM2NCH													
813h	CM2PCH													
814h	Unimplemented Read as '0'													
81Eh														
81Fh	CMOUT													
820h	PWM1DCL													
821h	PWM1DCH													
822h	PWM1CON													
823h	PWM2DCL													
824h	PWM2DCH													
825h	PWM2CON													
826h	Unimplemented Read as '0'													
82Eh														
82Fh	PWMTMRS0													
830h	Unimplemented Read as '0'								A20h						
86Fh		8EFh		96Fh		9EFh		A6Fh		AEFh		B6Fh		BEFh	
870h	Common RAM (Accesses 70h-7Fh)	8F0h	Common RAM (Accesses 70h-7Fh)	970h	Common RAM (Accesses 70h-7Fh)	9F0h	Common RAM (Accesses 70h-7Fh)	A70h	Common RAM (Accesses 70h-7Fh)	AF0h	Common RAM (Accesses 70h-7Fh)	B70h	Common RAM (Accesses 70h-7Fh)	BF0h	Common RAM (Accesses 70h-7Fh)
87Fh		8FFh		97Fh		9FFh		A7Fh		AFFh		B7Fh		BFFh	

Legend:

Unimplemented data memory locations, read as '0'