

BANK 60

1E00h	Core Registers	1E41h	CK1PPS
1E0Bh		1E42h	RX1PPS
1E0Ch	PPSLOCK	1E43h	Unimplemented Read as '0'
1E0Dh	INTPPS	1E46h	
1E0Eh	T0CKIPPS	1E47h	SSP1CLKPPS
1E0Fh	T1CKIPPS	1E48h	SSP1DATPPS
1E10h	T1GPPS	1E49h	SSP1SSPPS
1E11h	Unimplemented Read as '0'	1E4Ah	Unimplemented Read as '0'
1E18h		1E4Fh	
1E19h	T2INPPS	1E50h	ADACTPPS
1E1Ah	Unimplemented Read as '0'	1E51h	Unimplemented Read as '0'
1E1Dh		1E56h	
1E1Eh	CCP1PPS	1E57h	CLBIN0PPS
1E1Fh	CCP2PPS	1E58h	CLBIN1PPS
1E20h	Unimplemented Read as '0'	1E59h	CLBIN2PPS
1E3Ch		1E5Ah	CLBIN3PPS
1E3Dh	CLCIN0PPS	1E5Bh	Unimplemented Read as '0'
1E3Eh	CLCIN1PPS	1E6Fh	
1E3Fh	CLCIN2PPS	1E70h	Common RAM (Accesses 70h-7Fh)
1E40h	CLCIN3PPS	1E7Fh	

Legend:



Unimplemented data memory locations, read as '0'