

BANK 60

1E00h	Core Registers	1E3Dh	CLCIN0PPS
1E0Bh		1E3Eh	CLCIN1PPS
1E0Ch		1E3Fh	CLCIN2PPS
1E0Dh		1E40h	CLCIN3PPS
1E0Eh		1E41h	RX1PPS
1E0Fh		1E42h	CK1PPS
1E10h		1E43h	RX2PPS
1E11h		1E44h	CK2PPS
1E12h		1E45h	—
1E13h		1E46h	—
1E18h	Unimplemented Read as '0'	1E47h	SSP1CLKPPS
1E19h		1E48h	SSP1DATPPS
1E1Ah	T2INPPS	1E49h	SSP1SSPPS
1E1Bh	T4INPPS	1E4Ah	SSP2CLKPPS
1E1Ch	—	1E4Bh	SSP2DATPPS
1E1Dh	—	1E4Ch	SSP2SSPPS
1E1Eh	CCP1PPS	1E4Dh	Unimplemented Read as '0'
1E1Fh	CCP2PPS	1E4Fh	
1E20h	Unimplemented Read as '0'	1E50h	Unimplemented Read as '0'
1E38h		1E51h	
1E39h	CWG1PPS	1E6Fh	Common RAM (Accesses 70h-7Fh)
1E3Ah	Unimplemented Read as '0'	1E70h	
1E3Ch		1E7Fh	

Legend:



Unimplemented data memory locations, read as '0'