

BANK 61

1E80h	Core registers	1EA3h	SLRCONC ⁽²⁾
1E8Bh		1EA4h	INLVLC ⁽²⁾
1E8Ch	ANSELA	1EA5h	IOCCP ⁽²⁾
1E8Dh	WPUA	1EA6h	IOCCN ⁽²⁾
1E8Eh	ODCONA	1EA7h	IOCCF ⁽²⁾
1E8Fh	SLRCONA	1EA8h	Unimplemented Read as '0'
1E90h	INLVLA	1EE0h	
1E91h	IOCAP	1EE1h	RA1I2C ⁽³⁾
1E92h	IOCAN	1EE2h	RA2I2C ⁽³⁾
1E93h	IOCAF	1EE3h	—
1E94h	—	1EE4h	—
1E95h	—	1EE5h	RB4I2C ⁽¹⁾
1E96h	ANSELB ⁽¹⁾	1EE6h	RB5I2C ⁽¹⁾
1E97h	WPUB ⁽¹⁾	1EE7h	RB6I2C ⁽¹⁾
1E98h	ODCONB ⁽¹⁾	1EE8h	RB7I2C ⁽¹⁾
1E99h	SLRCONB ⁽¹⁾	1EE9h	RC0I2C ⁽⁴⁾
1E9Ah	INLVLB ⁽¹⁾	1EEAh	RC1I2C ⁽⁴⁾
1E9Bh	IOCBP ⁽¹⁾	1EEBh	—
1E9Ch	IOCBN ⁽¹⁾	1EECh	RC4I2C ⁽⁴⁾
1E9Dh	IOCBF ⁽¹⁾	1EEDh	RC5I2C ⁽⁴⁾
1E9Eh	—	1EEEh	—
1E9Fh	—	1EEFh	—
1EA0h	ANSEL ⁽²⁾	1EF0h	Common RAM (Accesses 70h-7Fh)
1EA1h	WPUC ⁽²⁾	1EFFh	
1EA2h	ODCONC ⁽²⁾		

Note:

1. 20-pin devices only
2. 14/16/20-pin devices only
3. PIC16F18015 only
4. PIC16F18025 only

Legend:



Unimplemented data memory locations, read as '0'