AT28LV010: Package Marking Information (Industrial Grade)

32-lead TSOP		32-lead PLCC	
Topside	Backside	Topside	Backside
•		•	
ATMEL AT28LV010		ATMEL AT28LV010	
20U-19506V		20U-19506V	
YYWWNNN		YYWWNNN	

Note: No backside marking on these device packages.

Lot Trace Code	
YWWNNN: Lot Trace Code Y: Year, WW: Work Week, NNN: Assembly Trace Code	