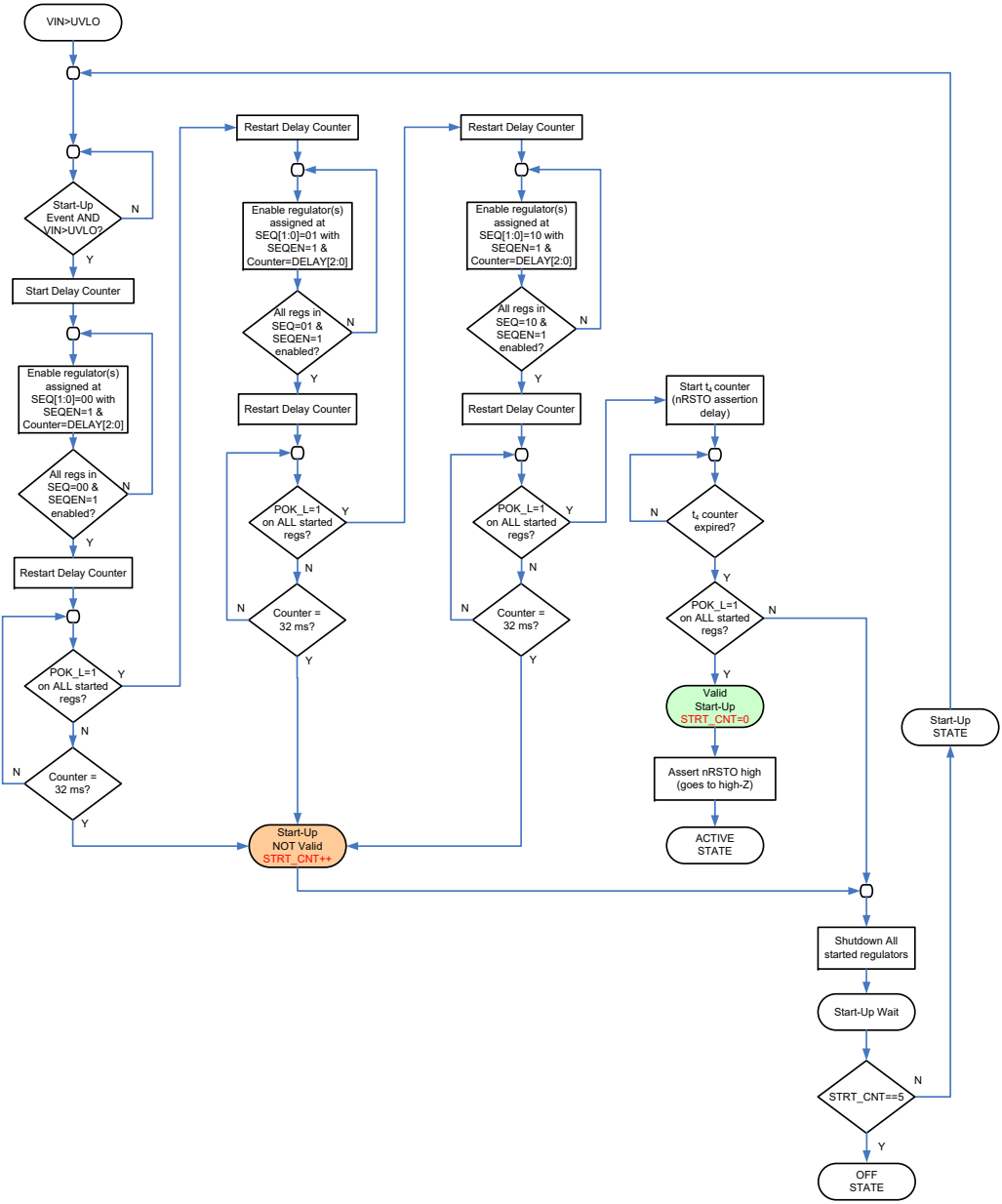
VIN>UVLO
Start-Up Event AND VIN>UVLO?
N
Y
Start Delay Counter
Enable regulator(s) assigned at SEQ[1:0]=00 with SEQEN=1 & Counter=DELAY[2:0]
All regs in SEQ=00 & SEQEN=1 enabled?
N
Y
Restart Delay Counter
POK_L=1 on ALL started regs?
Y
N
Counter = 32 ms?
N
Y
Restart Delay Counter
Enable regulator(s) assigned at SEQ[1:0]=01 with SEQEN=1 & Counter=DELAY[2:0]
All regs in SEQ=01 & SEQEN=1 enabled?
N
Y
Restart Delay Counter
POK_L=1 on ALL started regs?
Y
N
Counter = 32 ms?
N
Y
Restart Delay Counter
Enable regulator(s) assigned at SEQ[1:0]=10 with SEQEN=1 & Counter=DELAY[2:0]
All regs in SEQ=10 & SEQEN=1 enabled?
N
Y
Restart Delay Counter
POK_L=1 on ALL started regs?
Y
N
Counter = 32 ms?
N
Y
Start t_d counter (nRSTO assertion delay)
t_d counter expired?
N
Y
POK_L=1 on ALL started regs?
N
Y
Valid Start-Up STRT_CNT=0
Assert nRSTO high (goes to high-Z)
ACTIVE STATE
Start-Up NOT Valid STRT_CNT++
Shutdown All started regulators
Start-Up Wait
STRT_CNT==5
N
Y
OFF STATE
Start-Up STATE