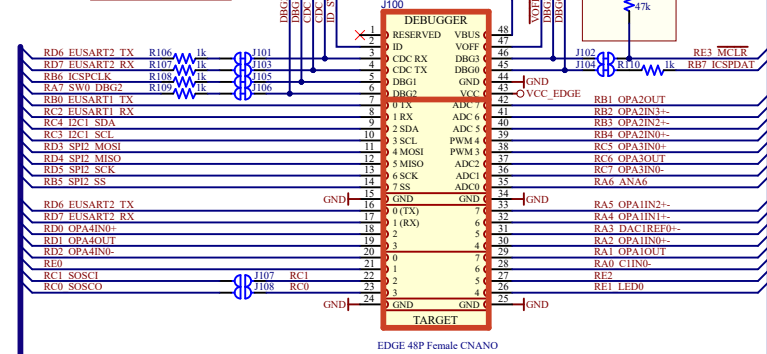


Debugger	Name	Pin
CDC TX	USARTn_RX	RD7
CDC RX	USARTn_TX	RD6
DBG0	ICSPDAT	RB7
DBG1	ICSPCLK	RB6
DBG2	GPIO0	RA7
DBG3	MCLR	RE3
VTG	1.8V - 5.5V	

CDC RX	CDC RX
CDC TX	CDC TX
DBG0	DBG0
DBG3	DBG3
DBG1	DBG1
DBG2	DBG2
VOFF	VOFF
ID SYS	ID SYS



RX/TX on the header denotes the input/output direction of the signal respective to it's source.

CDC TX is output from the DEBUGGER.
CDC RX is input to the DEBUGGER.
TX is output from the TARGET device.
RX is input to the TARGET device.

No pull-ups on board. Pull-ups must be mounted close to client device(s).

No pull-ups on board. Pull-ups must be mounted close to client device(s).

Crystal datasheet:
Ccrystal = 7pF
max ESR = 70kOhm
Accuracy ± 20 ppm

Selected in design after verification
C= 5.6pF/5.6pF

Selected in design after verification
C= 5.6pF/5.6pF

NANO Template Revision: 2.7

Project Owner: TF				CNA500 (Rev. January 2, 2017)
PCB Layout Contact: TF				
Part Number: EV14L29A	Project ID: PC116F 17576 Curiosity Nano	Variant: <u>Default Assembly</u>		
Sheet Title Target MCU		Designed with 		
Size: A3	SCH #: 02-012125 PCB #: 04-12189	Rev.: 1 Date: 3/19/2025	Sheet 2 of 4 Altium.com	
File: PC116F17576_Curiosity_Nano_Target_MCU1.SchDoc				