



Project settings



- Device selection
- Device settings
- Design flow
- Analysis operating conditions
- Simulation options
 - DO file
 - Waveforms
 - Vsim commands**
 - Timescale
- Simulation libraries
 - PolarFire
- General Settings
- Global Include Paths

SDF timing delays

- Minimum
- Typical
- Maximum

Disable pulse filtering during SDF based simulations

SDF Corner

- Slow process, Low voltage and High temperature
- Slow process, Low voltage and Low temperature
- Fast process, High voltage and Low temperature

Save

Restore Defaults

Resolution:

Additional options:

Pli Path: ...