

JTAG or SPI Slave UJTAG is enabled.
JTAG (1149.1) boundary scan is enabled.
JTAG or SPI Slave reading of temperature and voltage sensor is enabled.

Microsemi Factory Access

Allow Microsemi factory test mode access. This is required to perform Failure Analysis on the device.

JTAG/SPI Slave User Commands Policy

PUF emulation is available.
External Fabric/sNVM design digest check requests through JTAG and SPI Slave are available.
External zeroization through JTAG/SPI Slave is available.

Security key mode

- Bitstream encryption with default key Custom security options

User keys

Update policy

Debug policy

Microsemi
factory
access

JTAG/SPI Slave
commands
policy

Microsemi factory test mode access is allowed by default.
This is required to perform Failure Analysis on the device.
Use [FlashLock/UPK1](#) to change access level.

Microsemi factory test mode access level

- Allow factory test mode access
 Disable factory test mode access

Reset to default

Help

Save Summary to File...

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Finish

Cancel