

Port Name :

Rise Width : 1ns

Bank Name/ Pin Number	Port Name	Instance Name	IO Standard	Drive Strength (mA)	Static	Don't Care	Noise Margin (%)	Within Guideline
Bank0 (1.5v)								
A16	wr	wr_obuf/U0/U_IOPAD	LVCN0515	4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	99.01	Yes
A15	datao[28]	datao_obuf[28]/U0/U_IOPAD	LVCN0515	4	<input type="checkbox"/>	<input type="checkbox"/>	99.01	Yes
Bank1 (3.3v)								
J28	datao[29]	datao_obuf[29]/U0/U_IOPAD	LVTTL	4	<input type="checkbox"/>	<input type="checkbox"/>	98.38	Yes
J29	datao[25]	datao_obuf[25]/U0/U_IOPAD	LVCN0533	4	<input type="checkbox"/>	<input type="checkbox"/>	98.38	Yes
Bank2 (2.5v)								
M30	datao[4]	datao_obuf[4]/U0/U_IOPAD	LVCN0525	12	<input type="checkbox"/>	<input type="checkbox"/>	-11.66	No
N26	address1[7]	address1_obuf[7]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-69.11	No
M28	address1[4]	address1_obuf[4]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-50.88	No
K30	address1[3]	address1_obuf[3]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-11.66	No
N27	address1[2]	address1_obuf[2]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-25.04	No
M27	address1[25]	address1_obuf[25]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-74.95	No
L28	address1[24]	address1_obuf[24]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-74.21	No
P23	address1[23]	address1_obuf[23]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-72.10	No
M26	address1[22]	address1_obuf[22]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-32.03	No
N25	address1[21]	address1_obuf[21]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-68.80	No
K28	address1[20]	address1_obuf[20]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-73.62	No
N24	address1[1]	address1_obuf[1]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-49.89	No
P24	address1[19]	address1_obuf[19]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-59.92	No
L29	address1[15]	address1_obuf[15]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-76.35	No
J27	address1[14]	address1_obuf[14]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-62.78	No
L30	address1[13]	address1_obuf[13]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	-75.03	No
P30	address1[12]	address1_obuf[12]/U0/U_IOP	LVCN0525	16	<input type="checkbox"/>	<input type="checkbox"/>	100.00	Yes
Bank3 (2.5v)								
AB29	mio	mio_obuf/U0/U_IOPAD	LVCN0525	6	<input type="checkbox"/>	<input type="checkbox"/>	70.52	Yes
W29	datao[20]	datao_obuf[20]/U0/U_IOPAD	LVCN0525	12	<input type="checkbox"/>	<input type="checkbox"/>	74.07	Yes
U25	datao[15]	datao_obuf[15]/U0/U_IOPAD	LVCN0525	12	<input type="checkbox"/>	<input type="checkbox"/>	68.75	Yes
V23	datao[14]	datao_obuf[14]/U0/U_IOPAD	LVCN0525	12	<input type="checkbox"/>	<input type="checkbox"/>	75.20	Yes