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Global Include Paths

## Global Include Paths

Include Paths Order: 

Specify the search paths for the include commands for the Verilog files that are in your project.

Example [ Path1 ; Path2 ; Path3 ]

The following include paths are supported:

- Absolute path
- Relative Path with respect to project directory { Example: hdl/GLOBAL\_INCLUDE\_PATH }
- Relative path with respect to environment variable { Example : \${ENV\_VAR}/GLOBAL\_INCLUDE\_PATH }

Note : This environment variable should be the same as the one used in Absolute/Relative Paths settings for linked files

Save

Discard